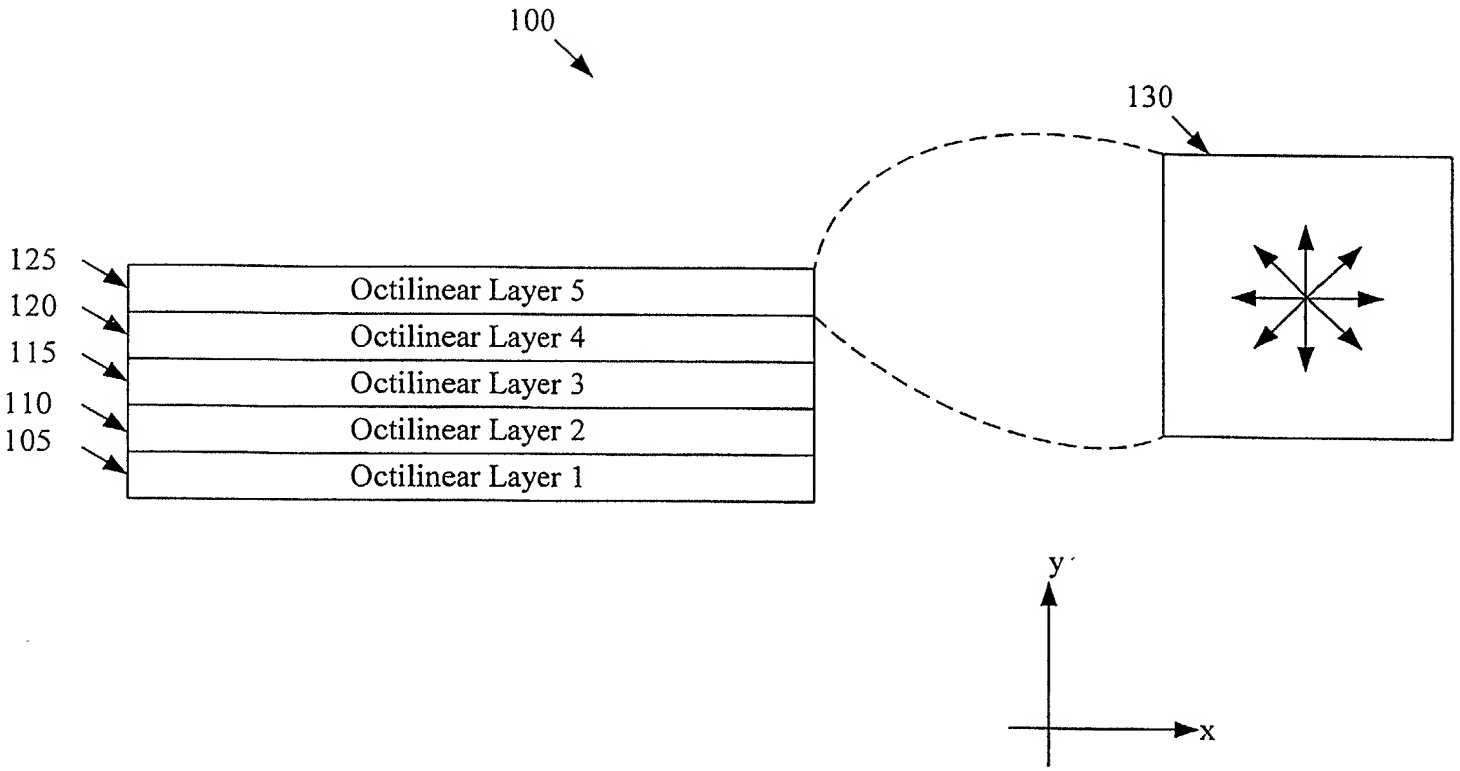

100
130
125
120
115
110
105
Octilinear Layer 5
Octilinear Layer 4
Octilinear Layer 3
Octilinear Layer 2
Octilinear Layer 1
y
x

Figure 1

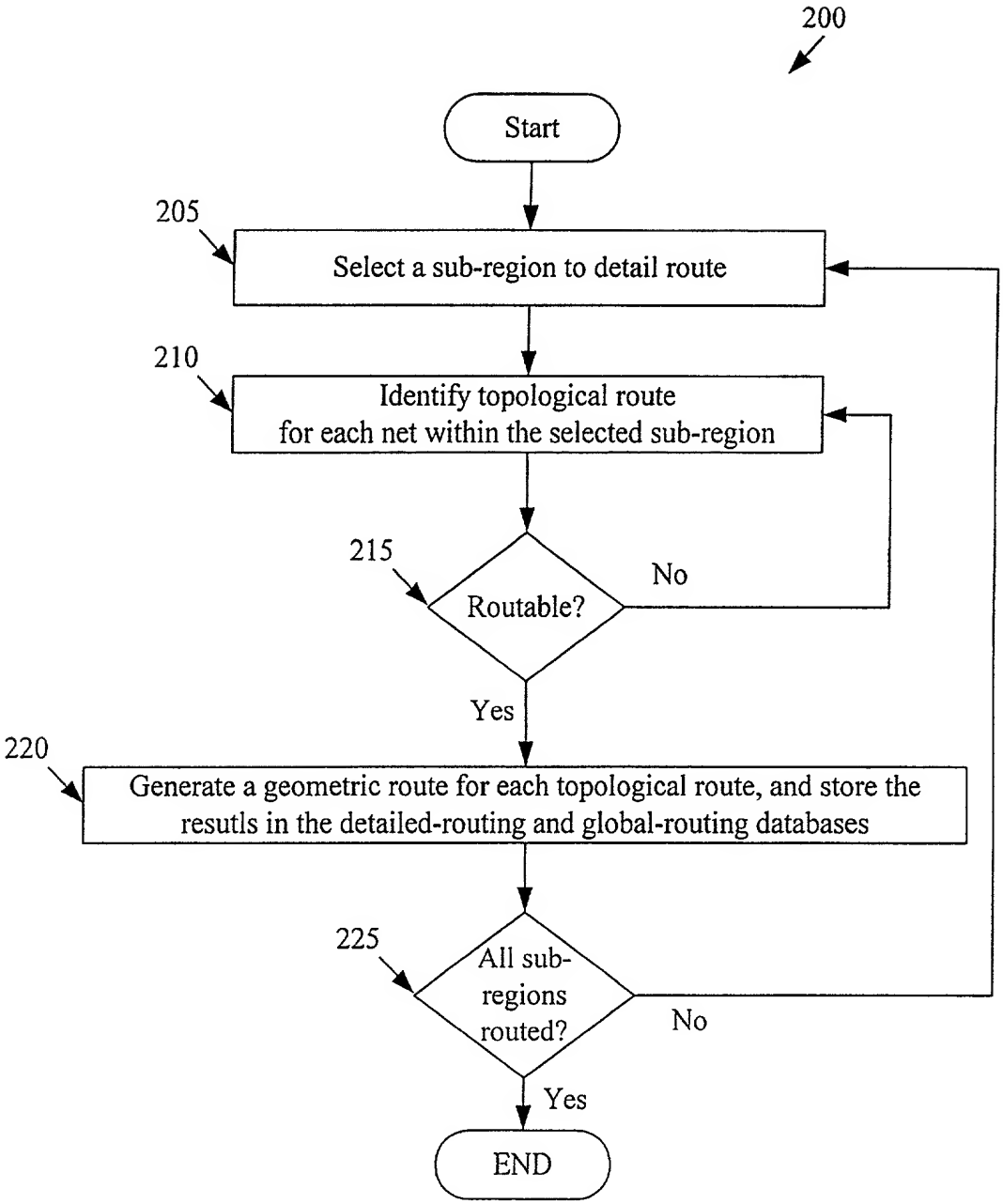

200
Start
205
Select a sub-region to detail route
210
Identify topological route
for each net within the selected sub-region
215
Routable?
No
Yes
220
Generate a geometric route for each topological route, and store the resutls in the detailed-routing and global-routing databases
225
All sub-regions routed?
No
Yes
END

Figure 2

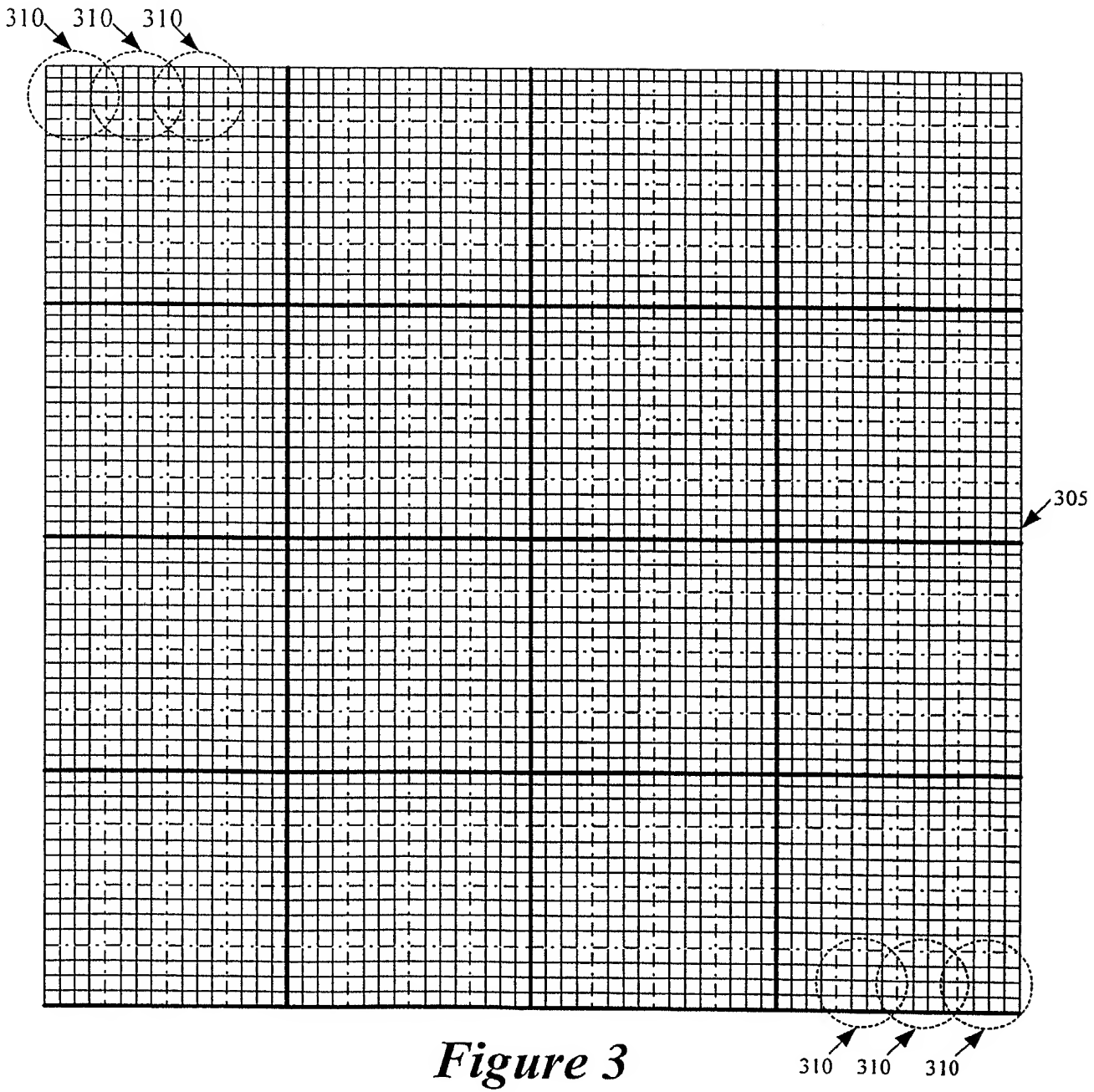


Figure 3

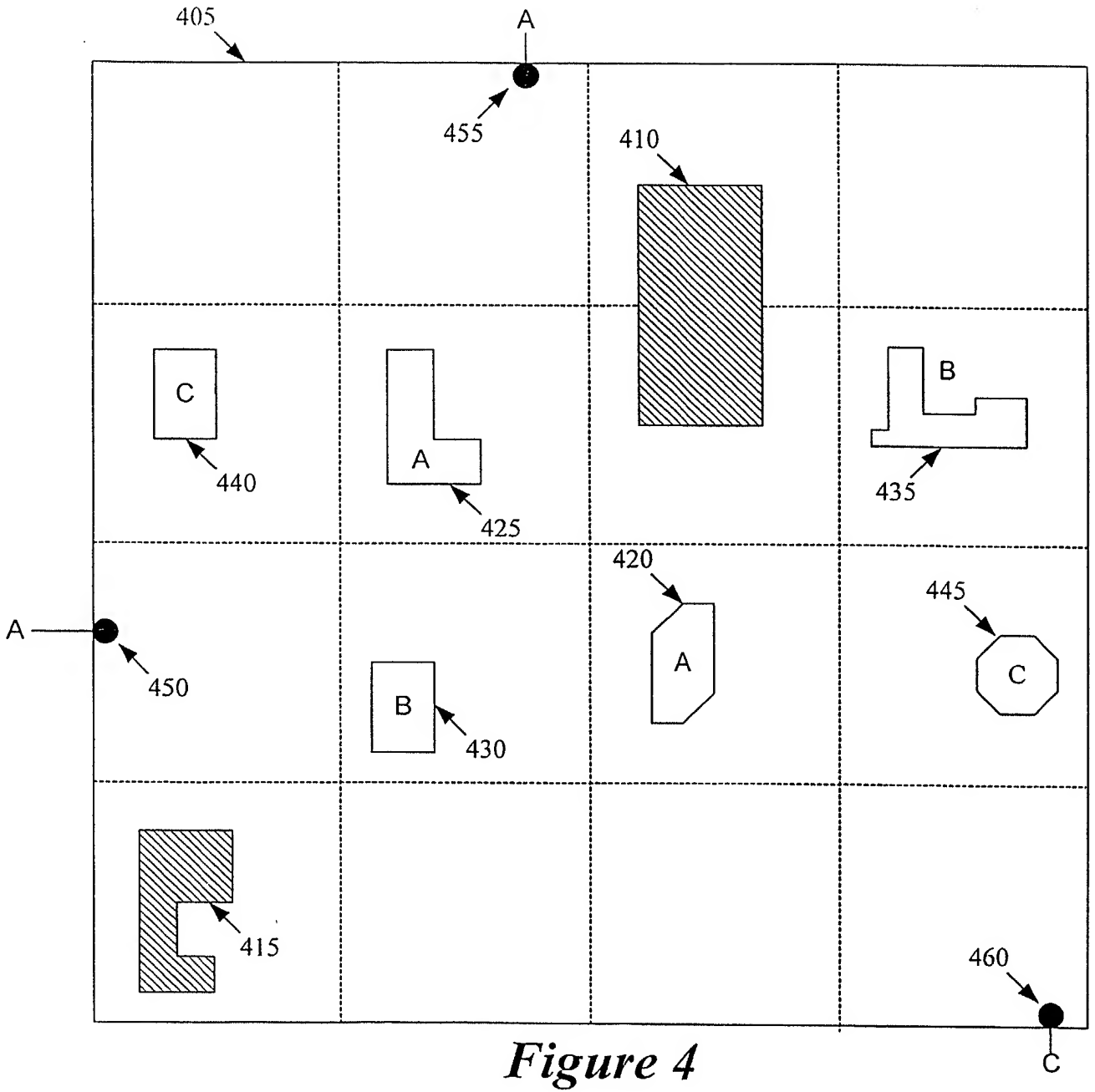


Figure 4

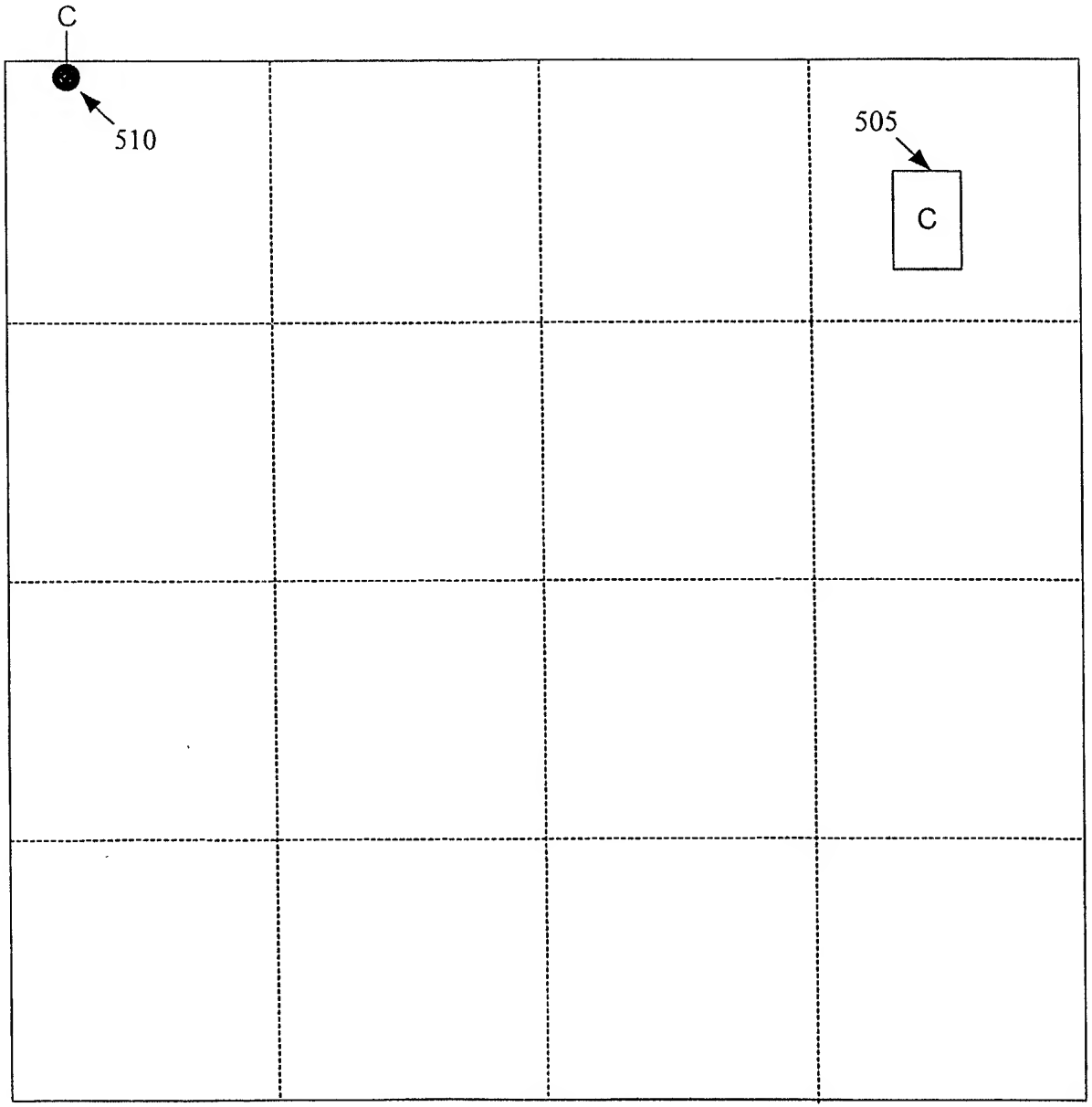


Figure 5

-List of Geometries
--Each Geometry including a sequence of points & layer assignment
-Bounding box of the region
-Array of layer properties
--Minimum wire size
--Minimum spacing
--Via sizes
--Cost/Unit
-Netlist specifying a number of nets
--Each net specifying a set of pins
--Each pin specifying a set of ports
--Each port specifying a set of geometries

Figure 6

-List of Geometries
--Each Geometry including a sequence of points & layer assignment
--List of connection nodes inside each pin geometry
-Bounding box of the region
-Array of layer properties
--Minimum wire size
--Minimum spacing
--Via sizes
--Cost/Unit
-Netlist specifying a number of nets
--Each net specifying a set of pins
--Each pin specifying a set of ports
--Each port specifying a set of geometries
-For each layer, a graph specifying
--Nodes
--Edges
--Faces

Figure 7

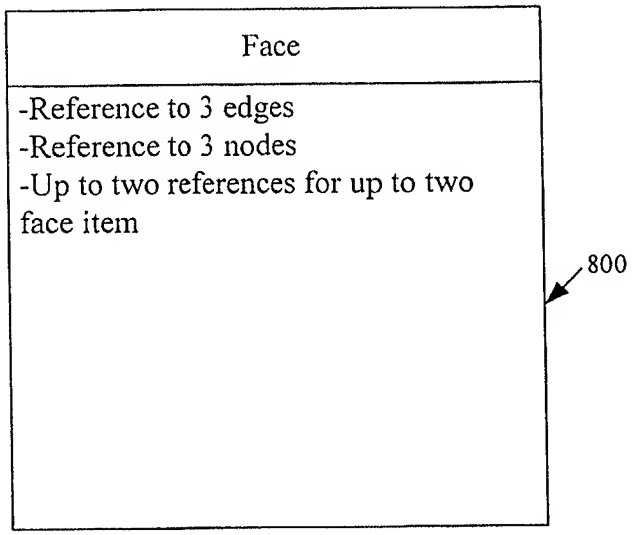


Figure 8

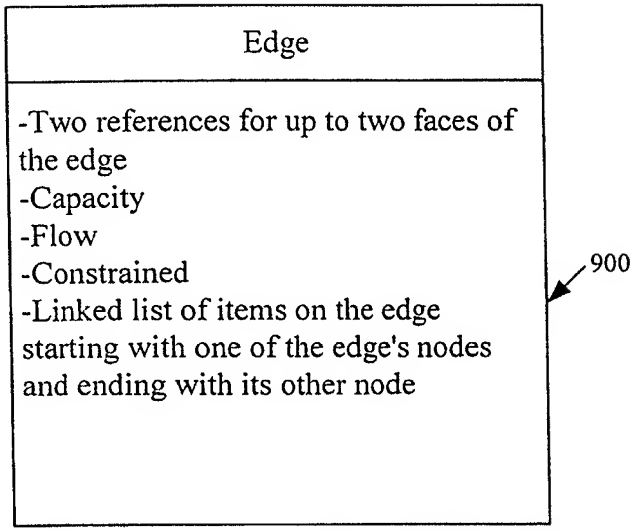


Figure 9

Node
-Net Identifier -One or more planar-path references to adjacent topological items in the same planar path -A pair of via-path references to up and down topological via items -A references to list of edges connected to the node -For each edge, an edge reference to the next or previous topological item on the edge -A reference to the geometry of the node -Vertex number identifying the vertex of the geometry -Location of the node

1000

Figure 10

Edge Item
-Reference to its edge -Net Identifier -A pair of planar-path references to adjacent topological items in the same planar path -A pair of edge references to the next and previous topological item on the edge

1100

Figure 11

Face Item
-Reference to its face -Net Identifier -Up to 3 planar-path references for adjacent topological items in the same planar path -A pair of via-path references for up and down topological via items -Bounding polygon that defines legal face item locations -Constraining Points and Distances

1200

Figure 12

20020050990A1

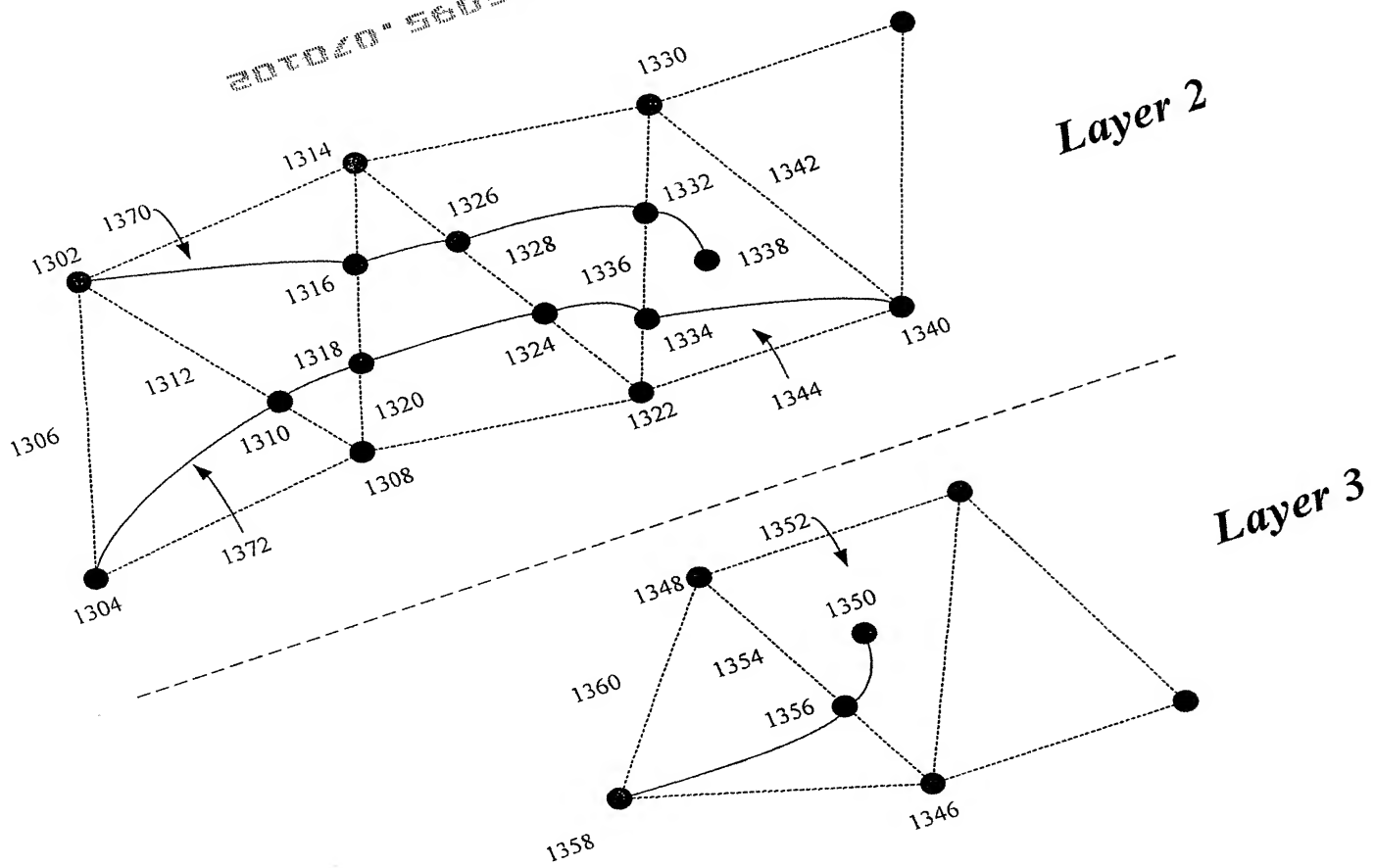
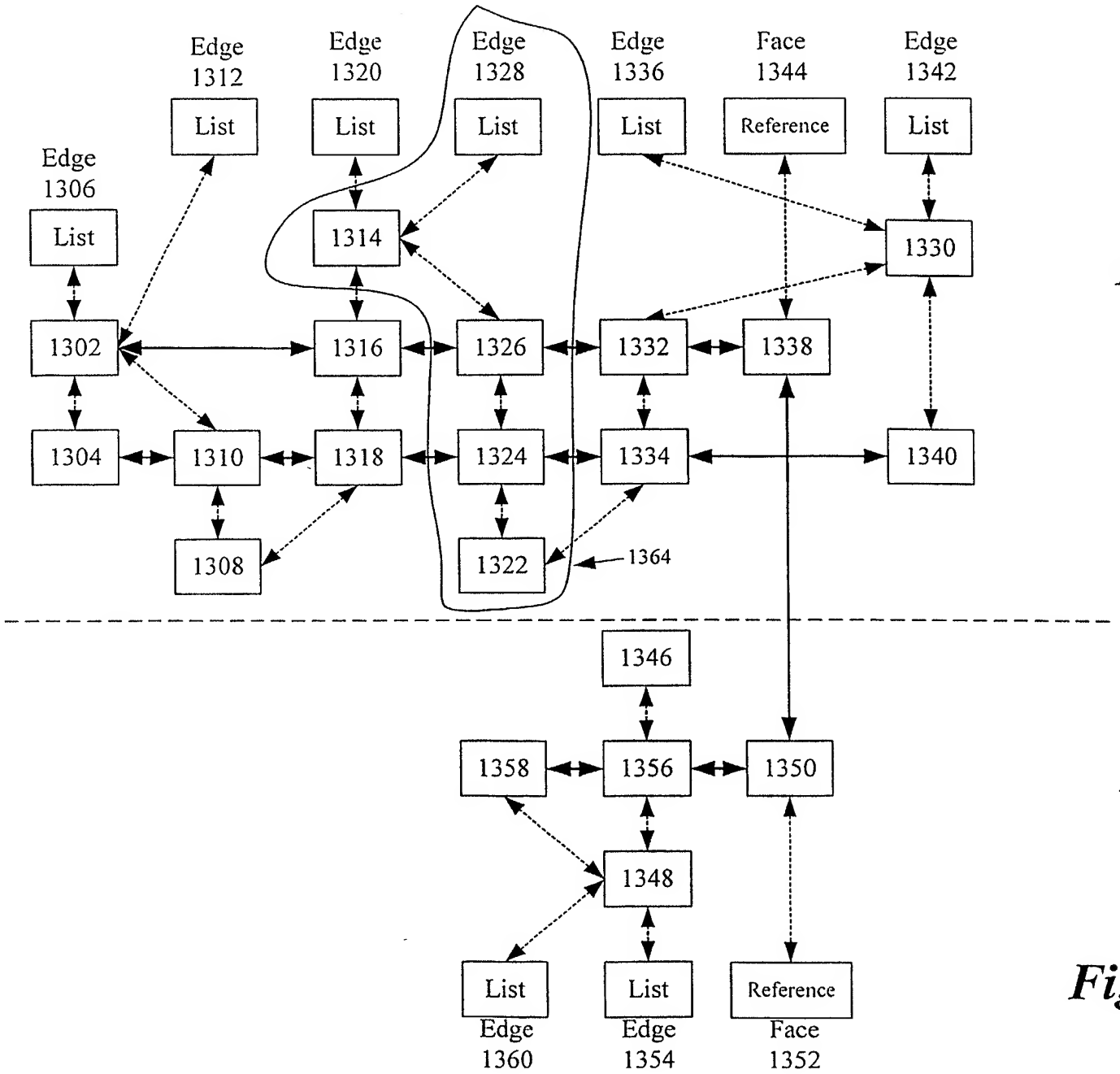


Figure 13



Layer 2

Layer 3

Figure 14

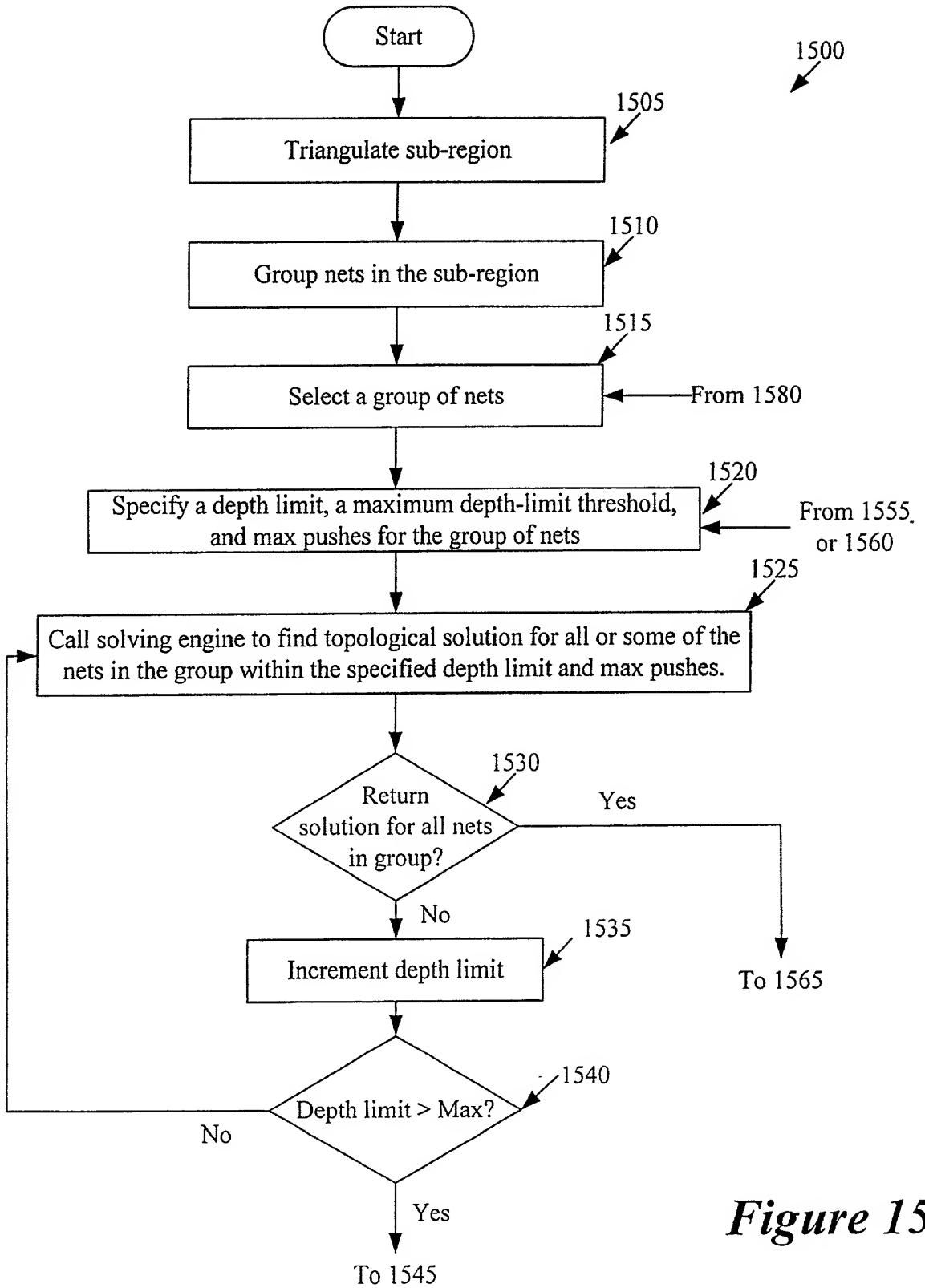


Figure 15A

Figure 15: Figure 15A / Figure 15B

20F9C9 "5939904"

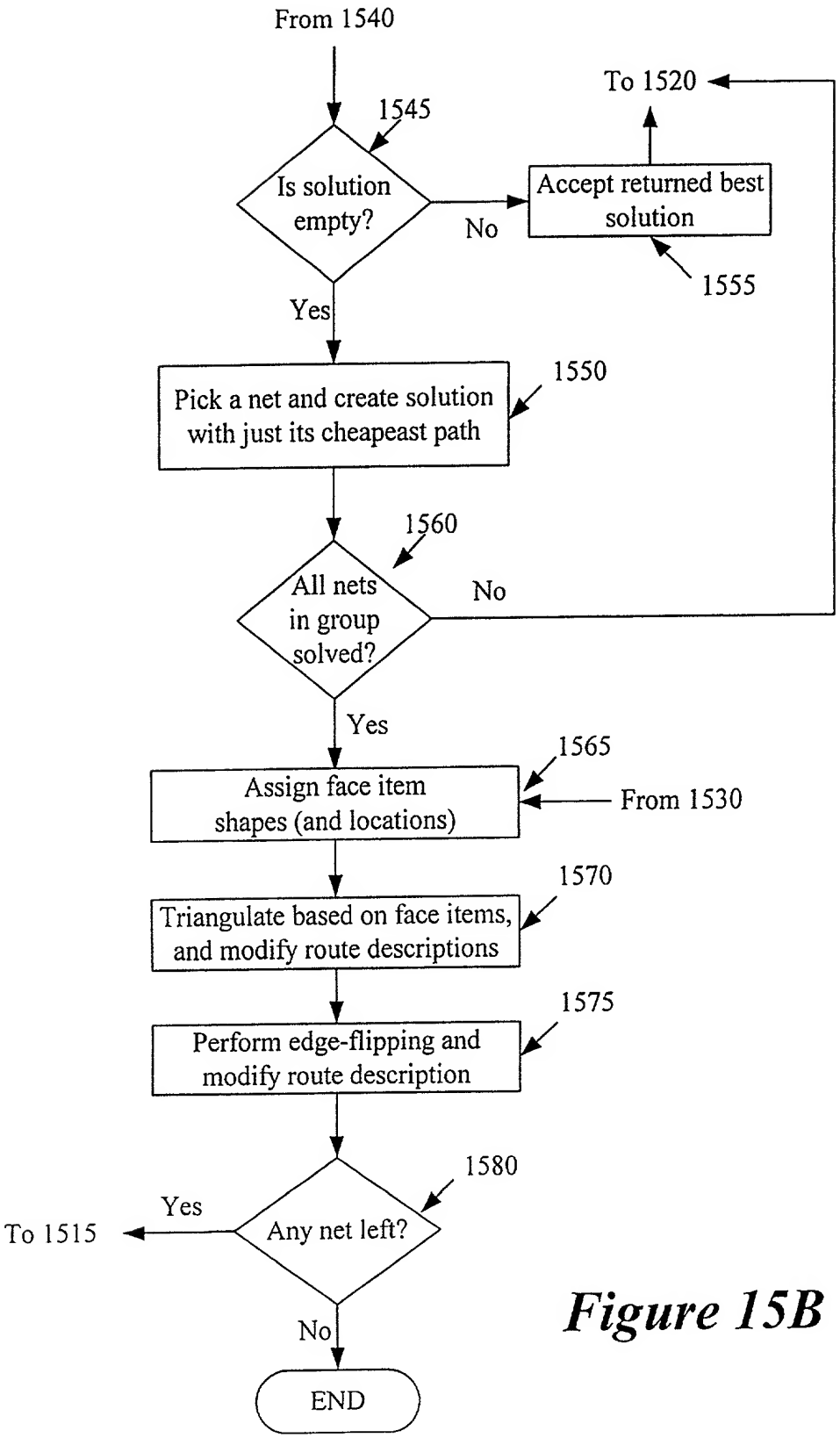


Figure 15B

20050256694

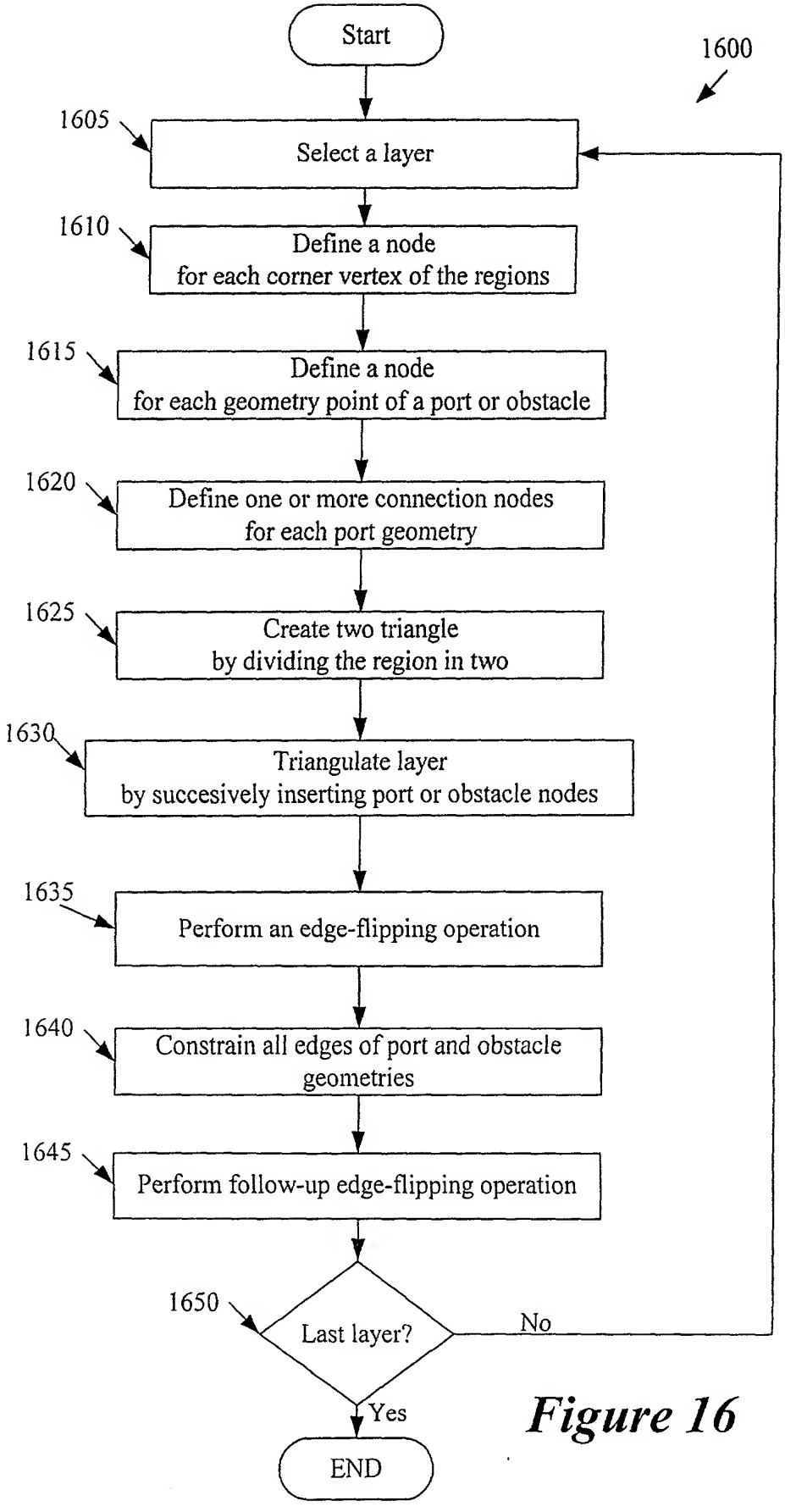


Figure 16

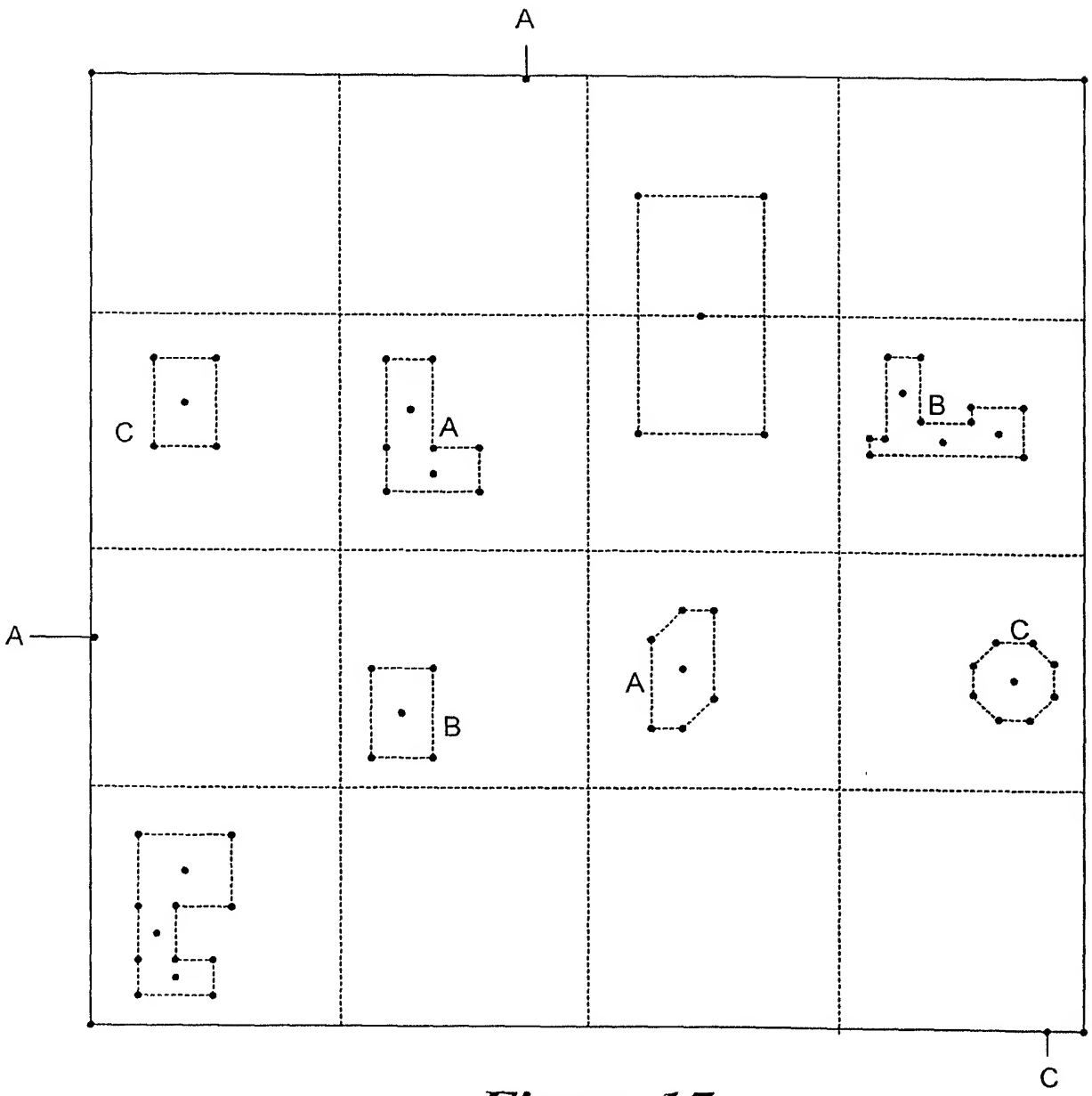


Figure 17

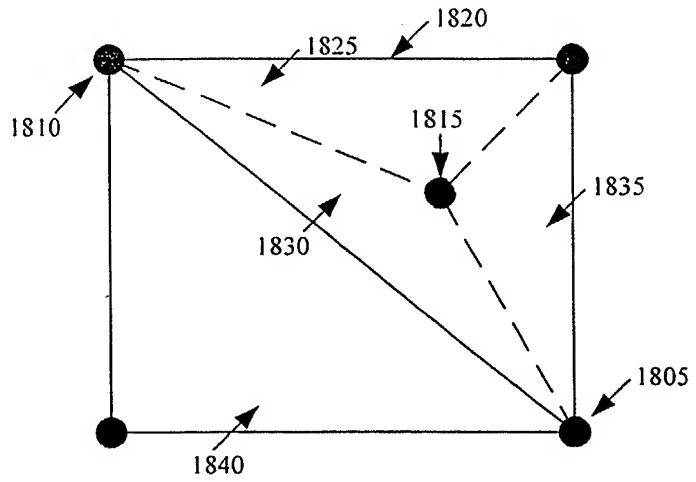


Figure 18

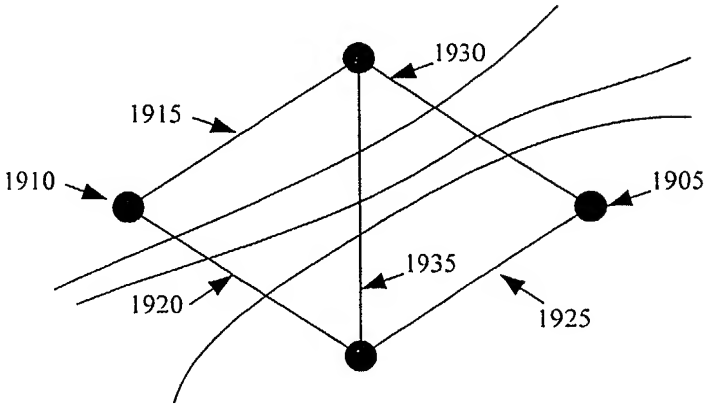


Figure 19

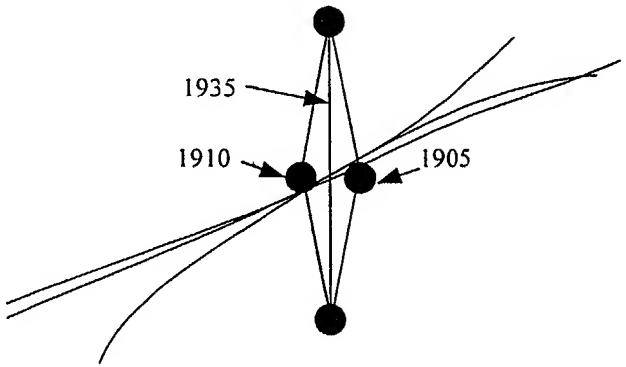


Figure 20

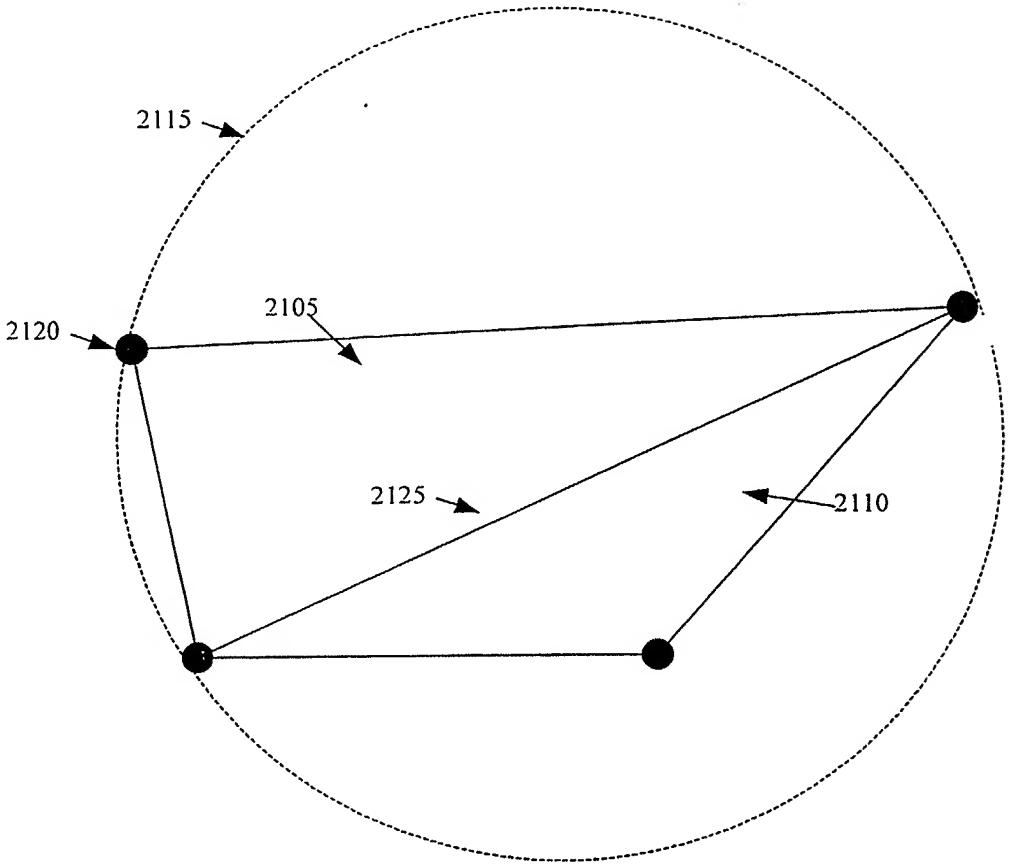


Figure 21

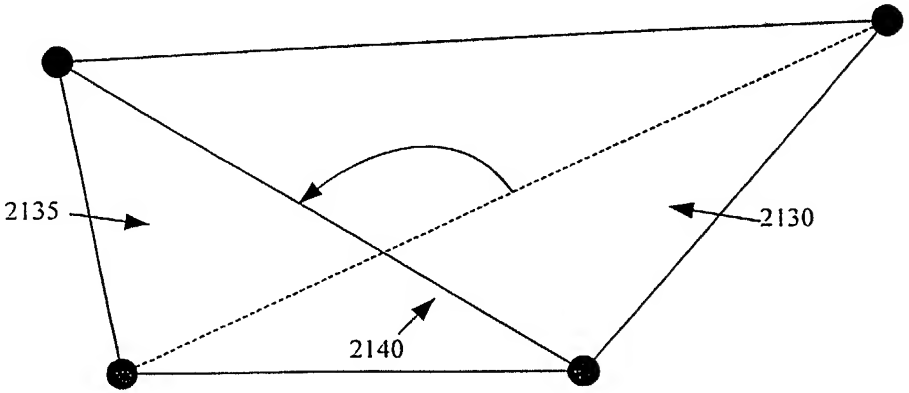


Figure 22

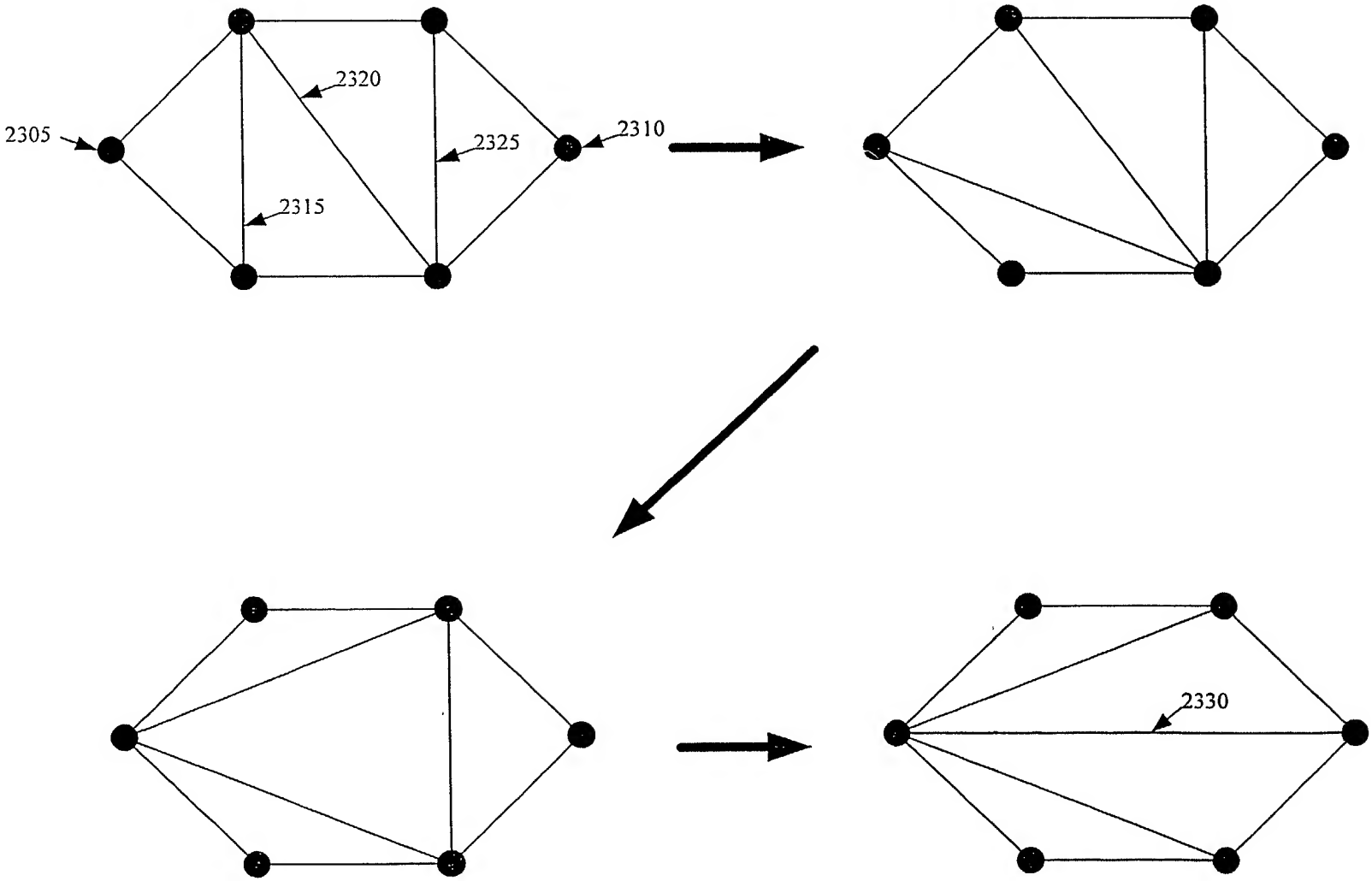


Figure 23

20020 "Sb0990t

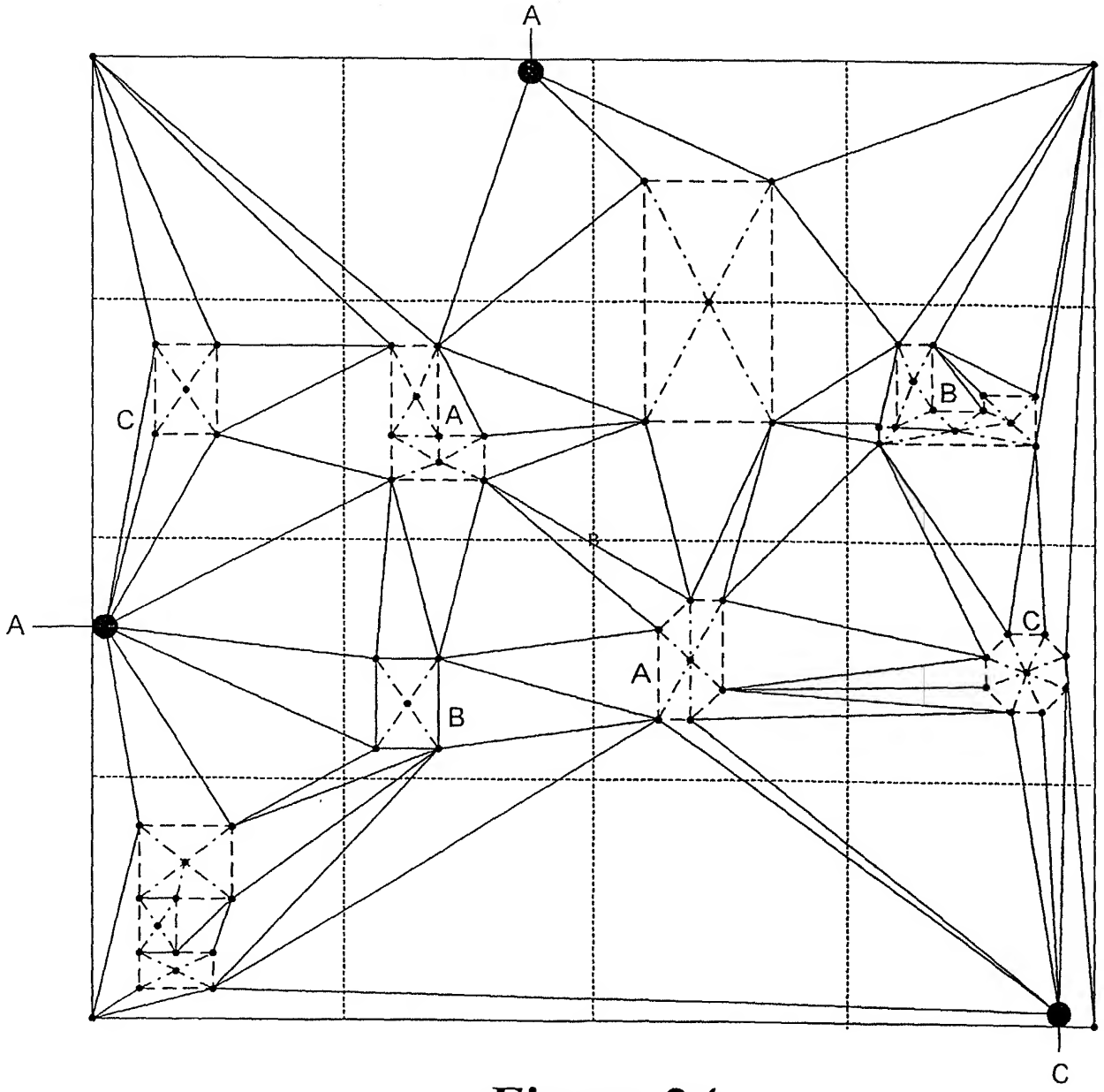


Figure 24

20050930.0500

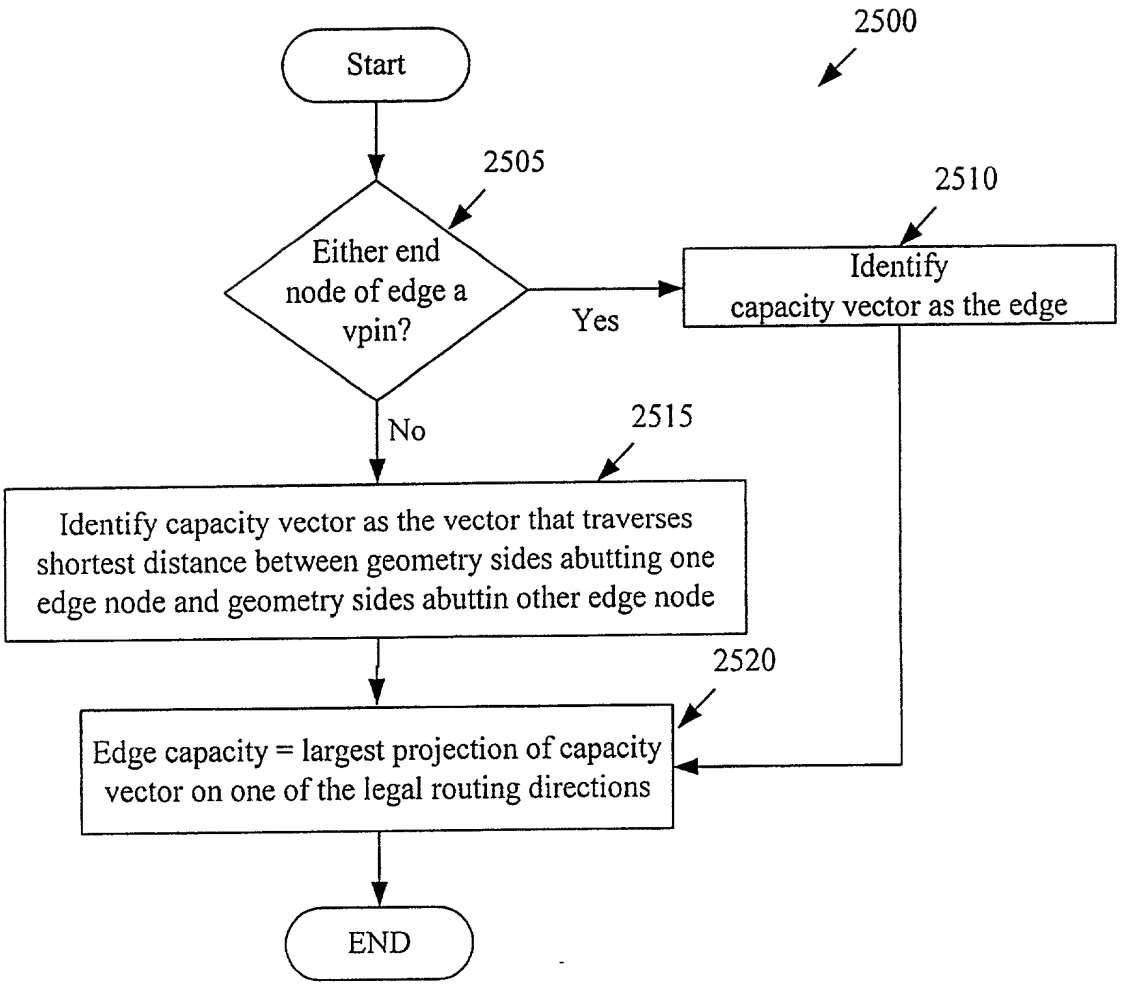


Figure 25

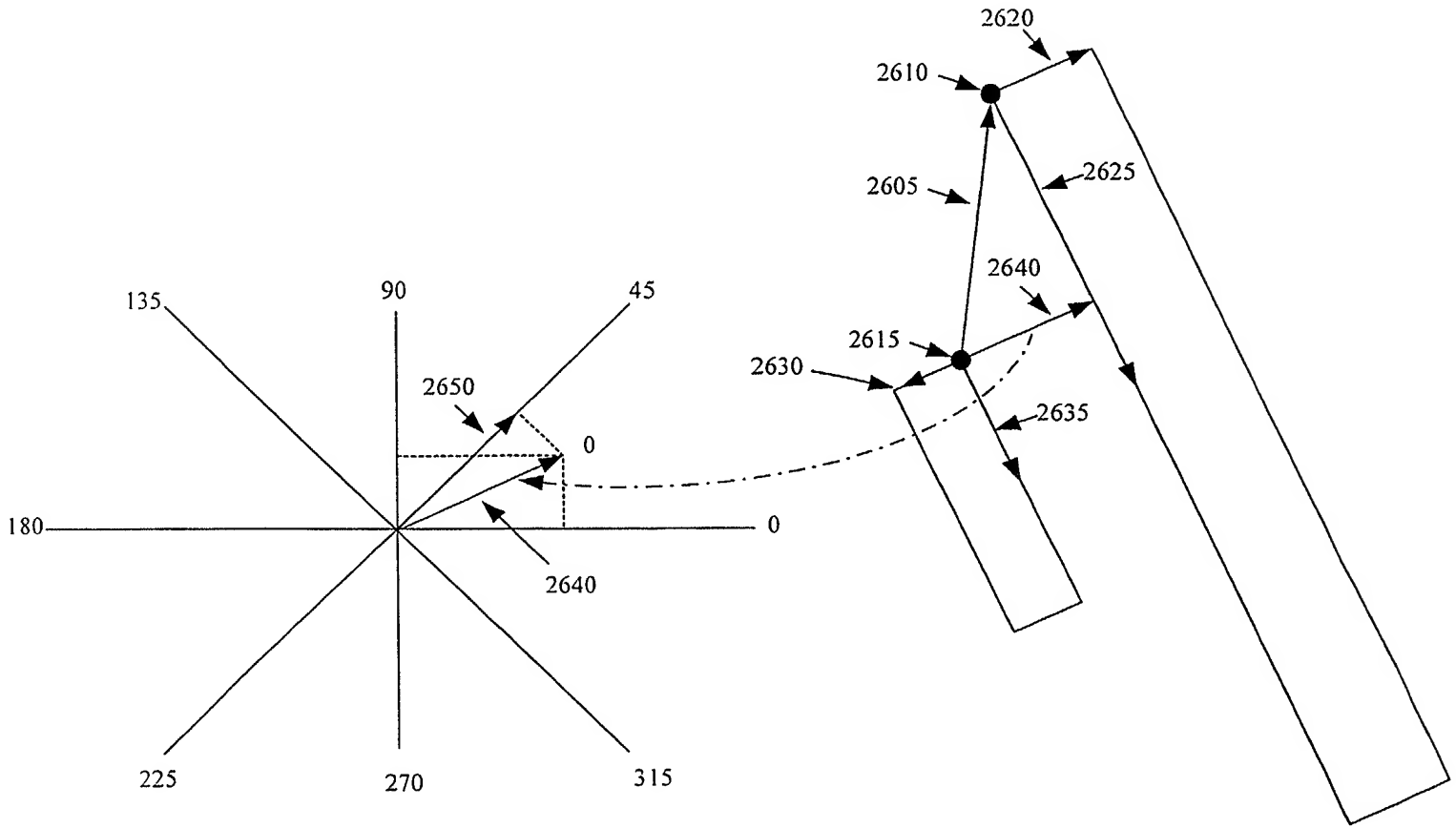

2620
2610
2605
2625
2640
2615
2630
2635
135
90
45
2650
0
180
0
2640
225
270
315

Figure 26

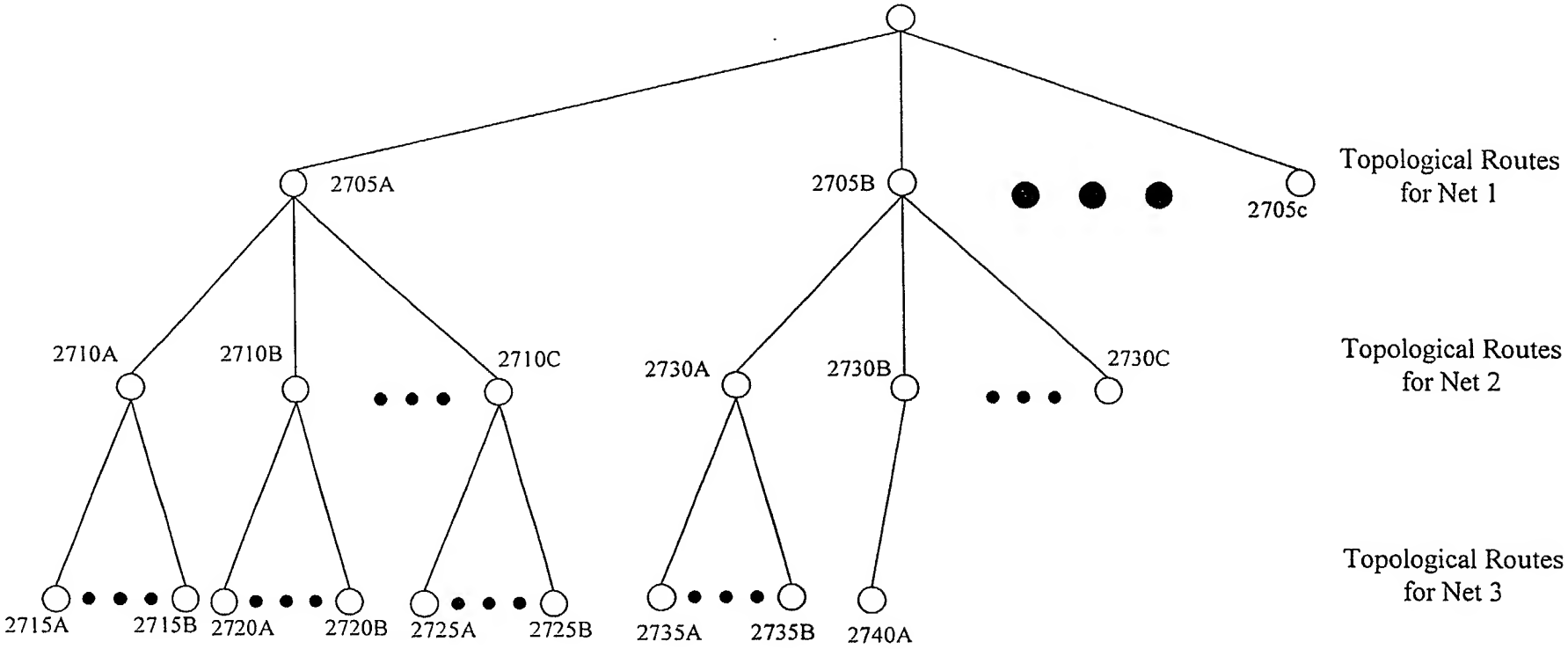

2705A
2705B
2705c
Topological Routes for Net 1
2710A
2710B
2710C
2730A
2730B
2730C
Topological Routes for Net 2
2715A
2715B
2720A
2720B
2725A
2725B
2735A
2735B
2740A
Topological Routes for Net 3

Figure 27

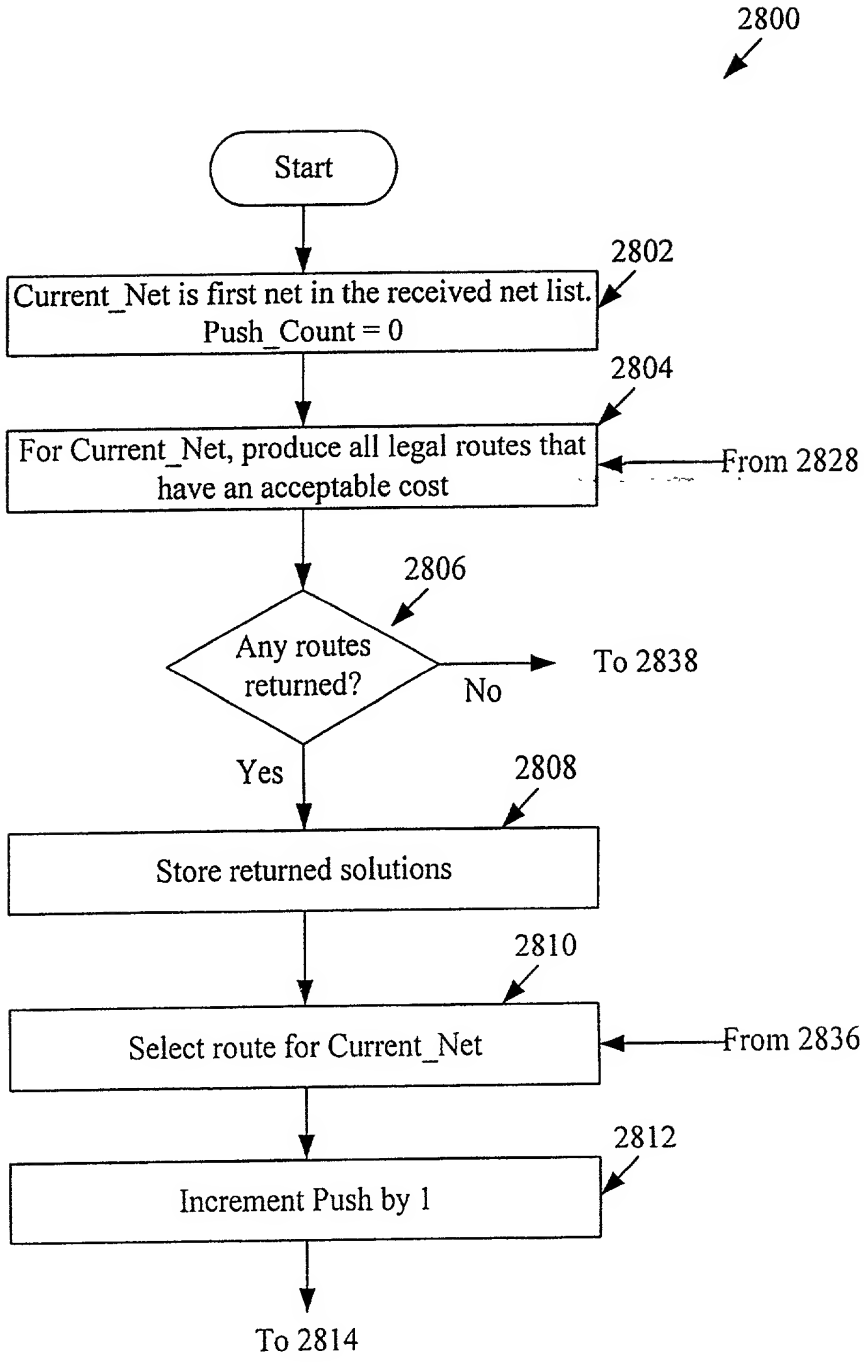


Figure 28A

Figure 28: *Figure 28A* / *Figure 28B* / *Figure 28C*

Express Mail Label No. EV339950705US

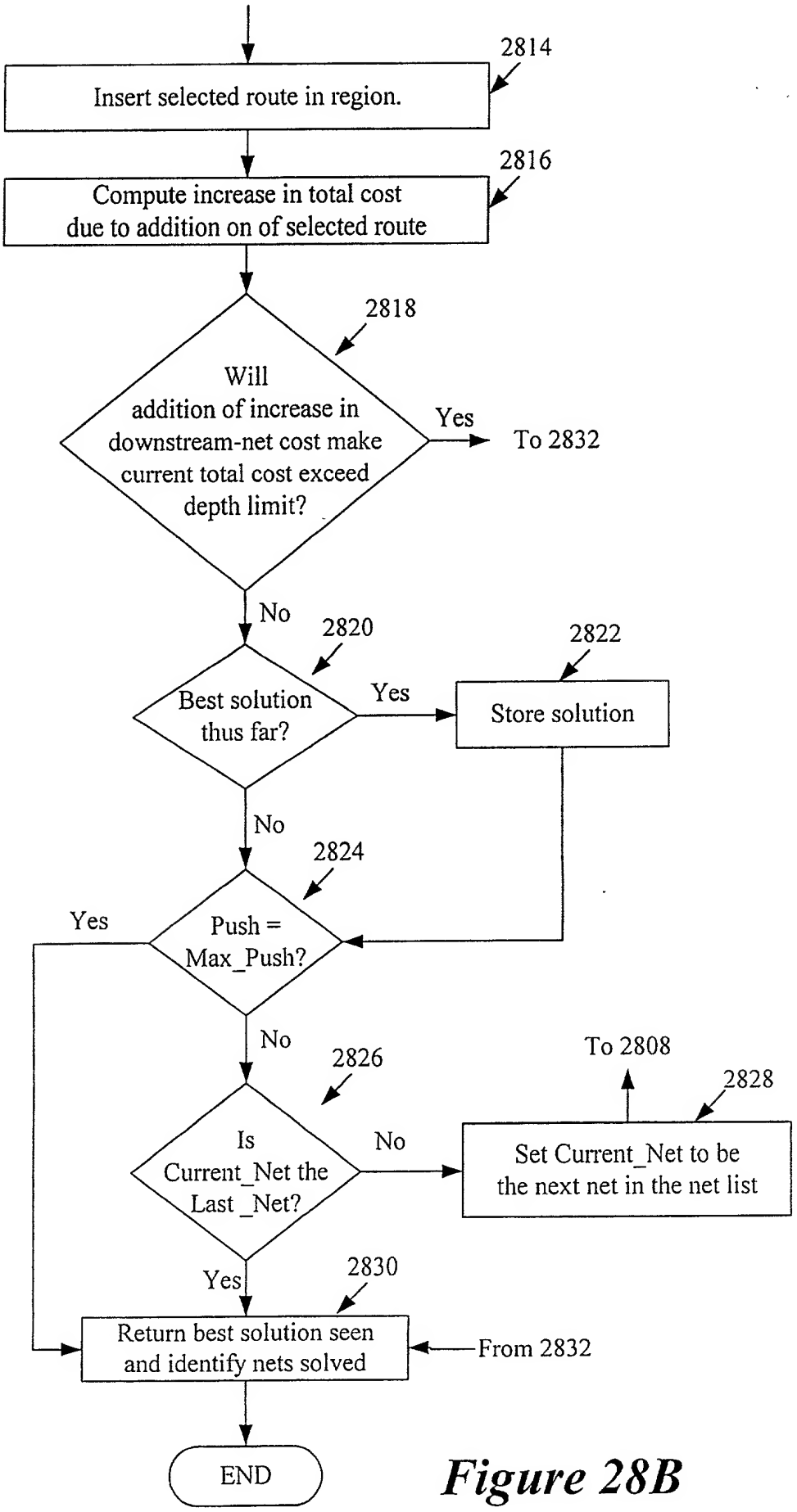


Figure 28B

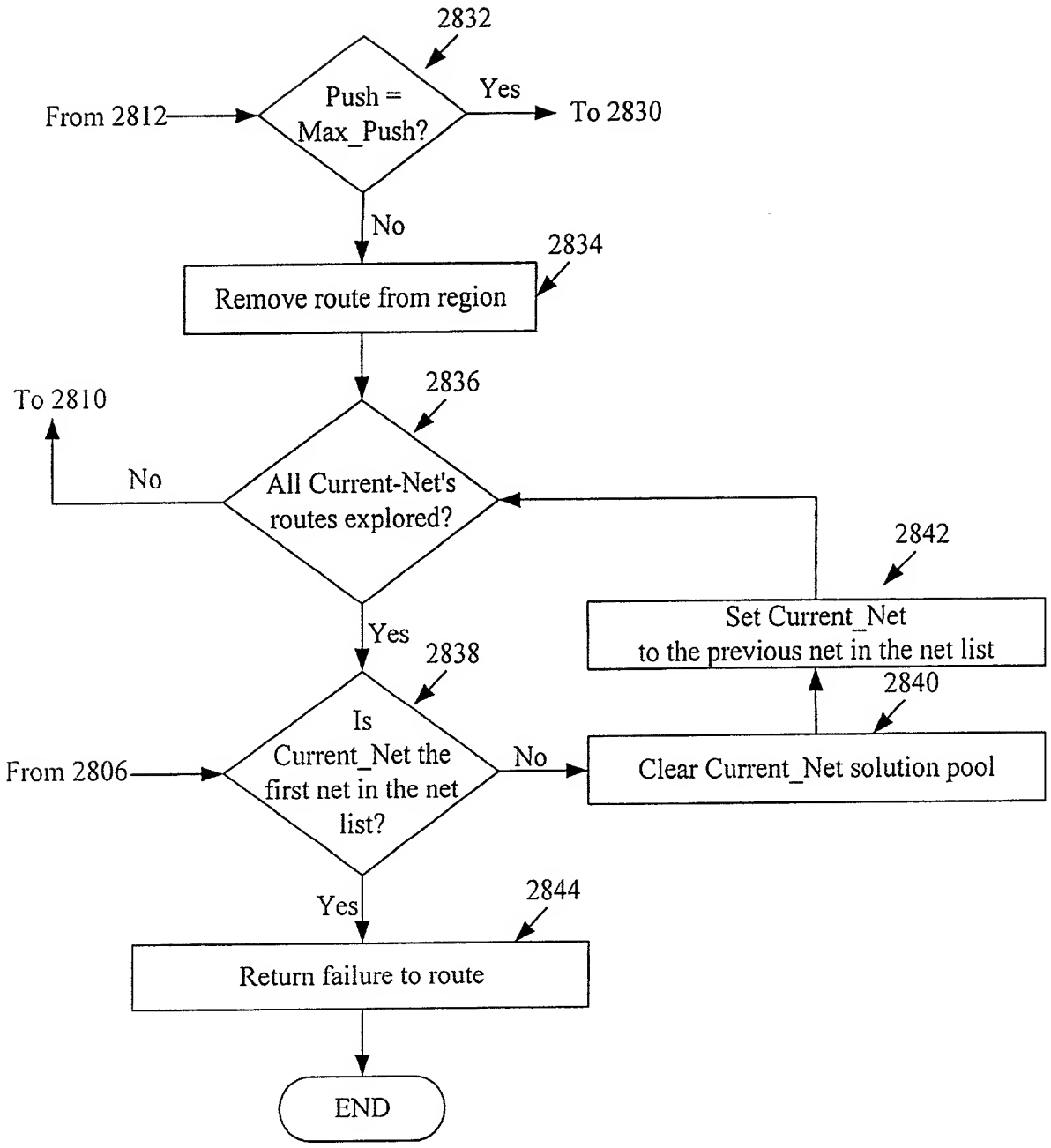

2832
From 2812
Push = Max_Push?
Yes
To 2830
No
2834
Remove route from region
2836
To 2810
No
All Current-Net's routes explored?
2842
Set Current_Net to the previous net in the net list
Yes
2838
2840
From 2806
Is Current_Net the first net in the net list?
No
Clear Current_Net solution pool
Yes
2844
Return failure to route
END

Figure 28C

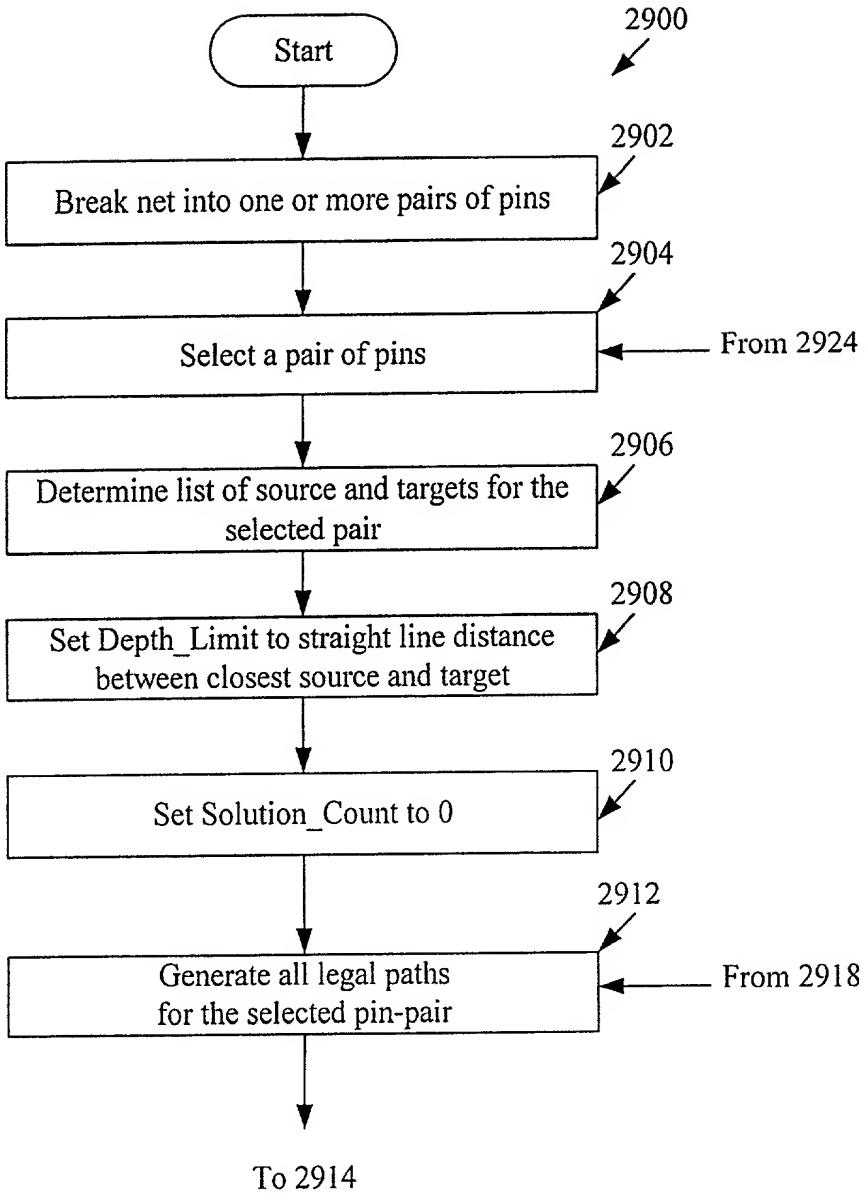


Figure 29A

Figure 29: *Figure 29A* / *Figure 29B*

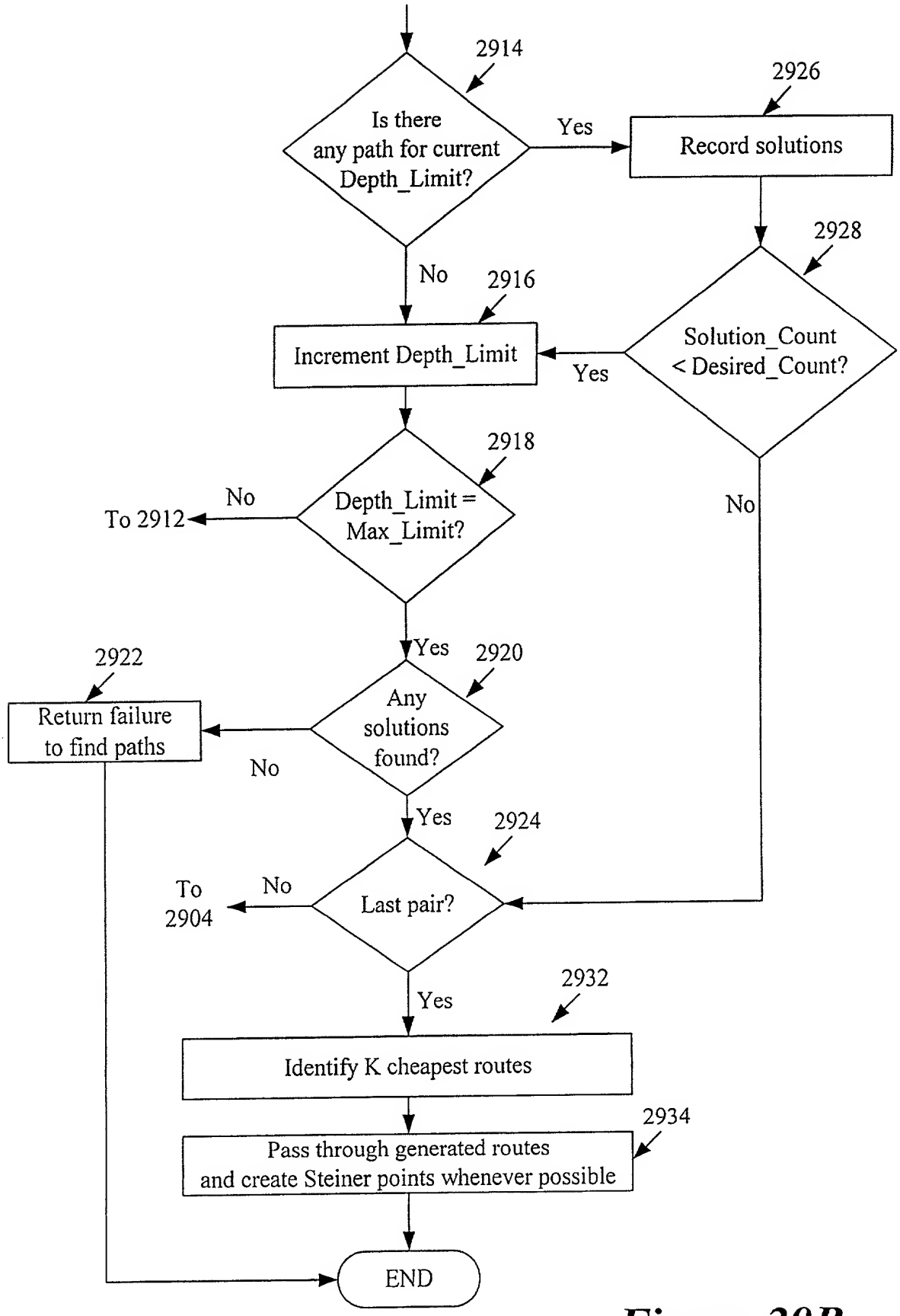

2914
Is there any path for current Depth_Limit?
Yes
2926
Record solutions
No
2916
Increment Depth_Limit
2928
Solution_Count < Desired_Count?
Yes
No
2918
Depth_Limit = Max_Limit?
No
To 2912
Yes
2920
Any solutions found?
2922
Return failure to find paths
No
Yes
2924
Last pair?
To 2904
No
Yes
2932
Identify K cheapest routes
2934
Pass through generated routes and create Steiner points whenever possible
END

Figure 29B

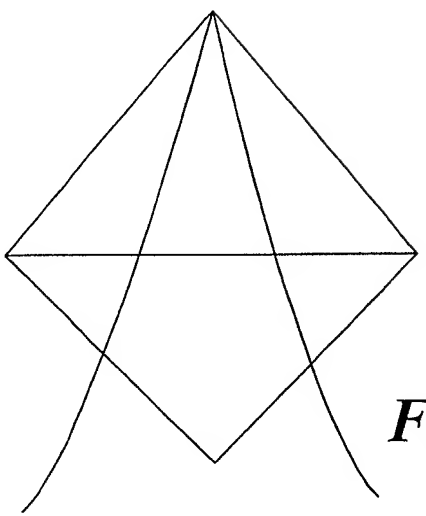


Figure 30A

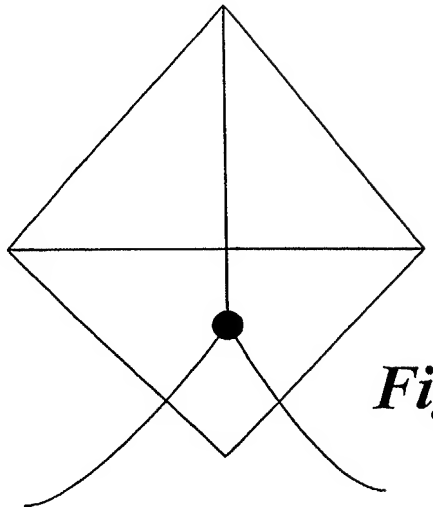


Figure 30B

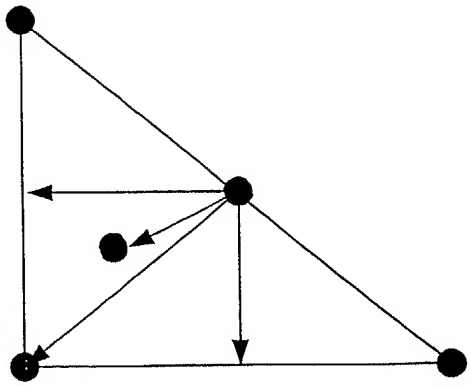


Figure 32

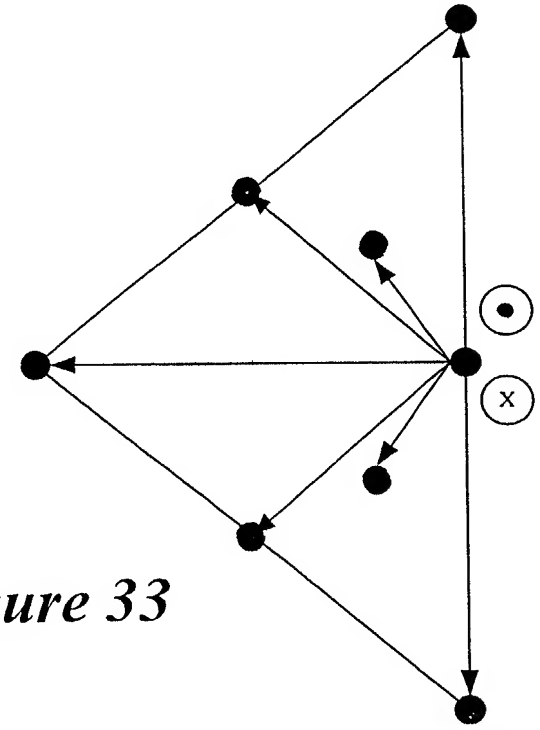


Figure 33

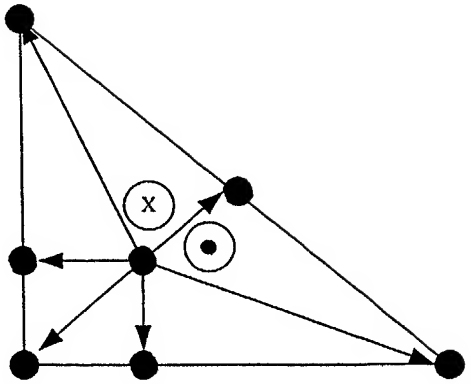


Figure 34

Docket: 5050-0004

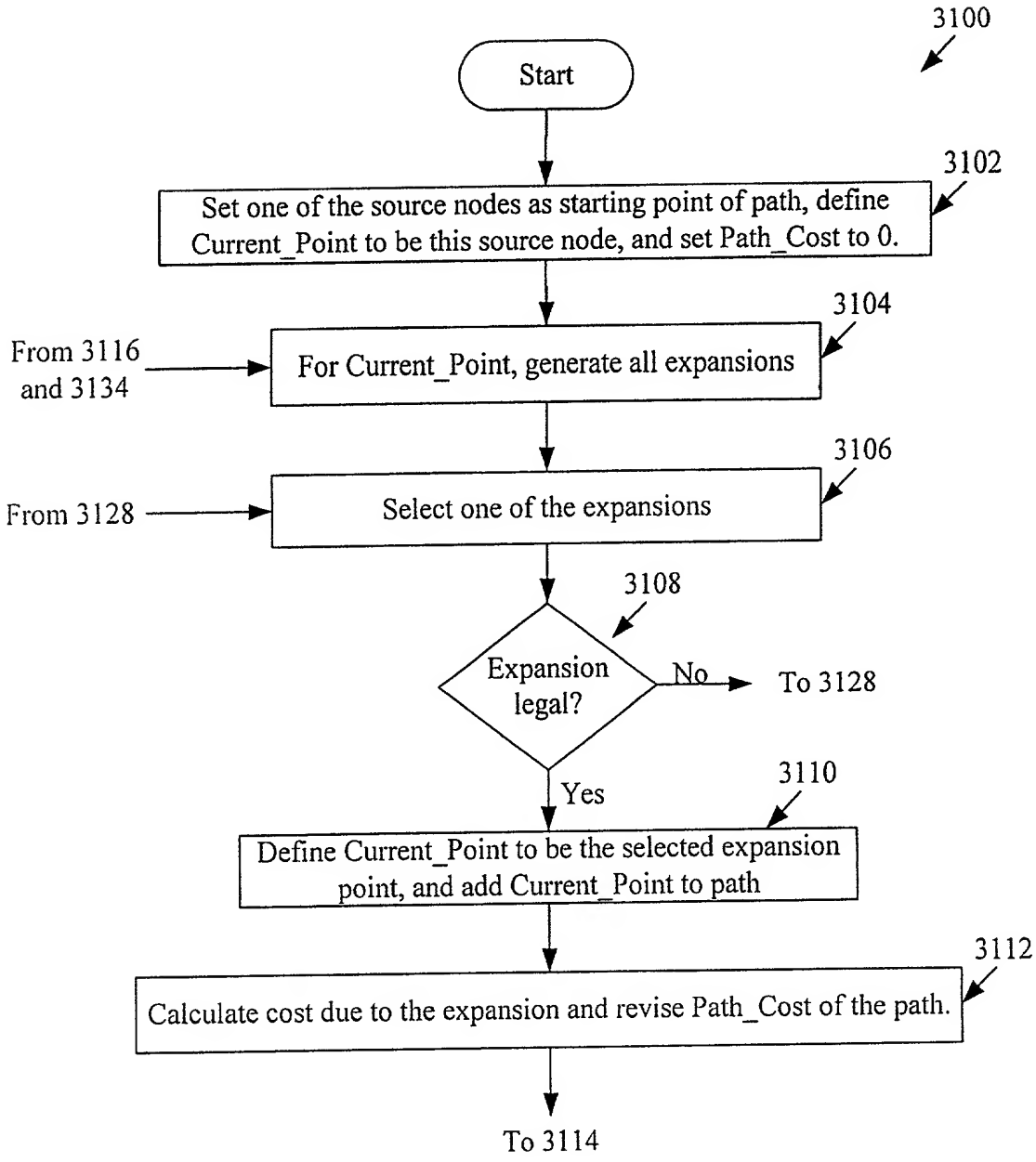


Figure 31A

Figure 31: Figure 31A / Figure 31B

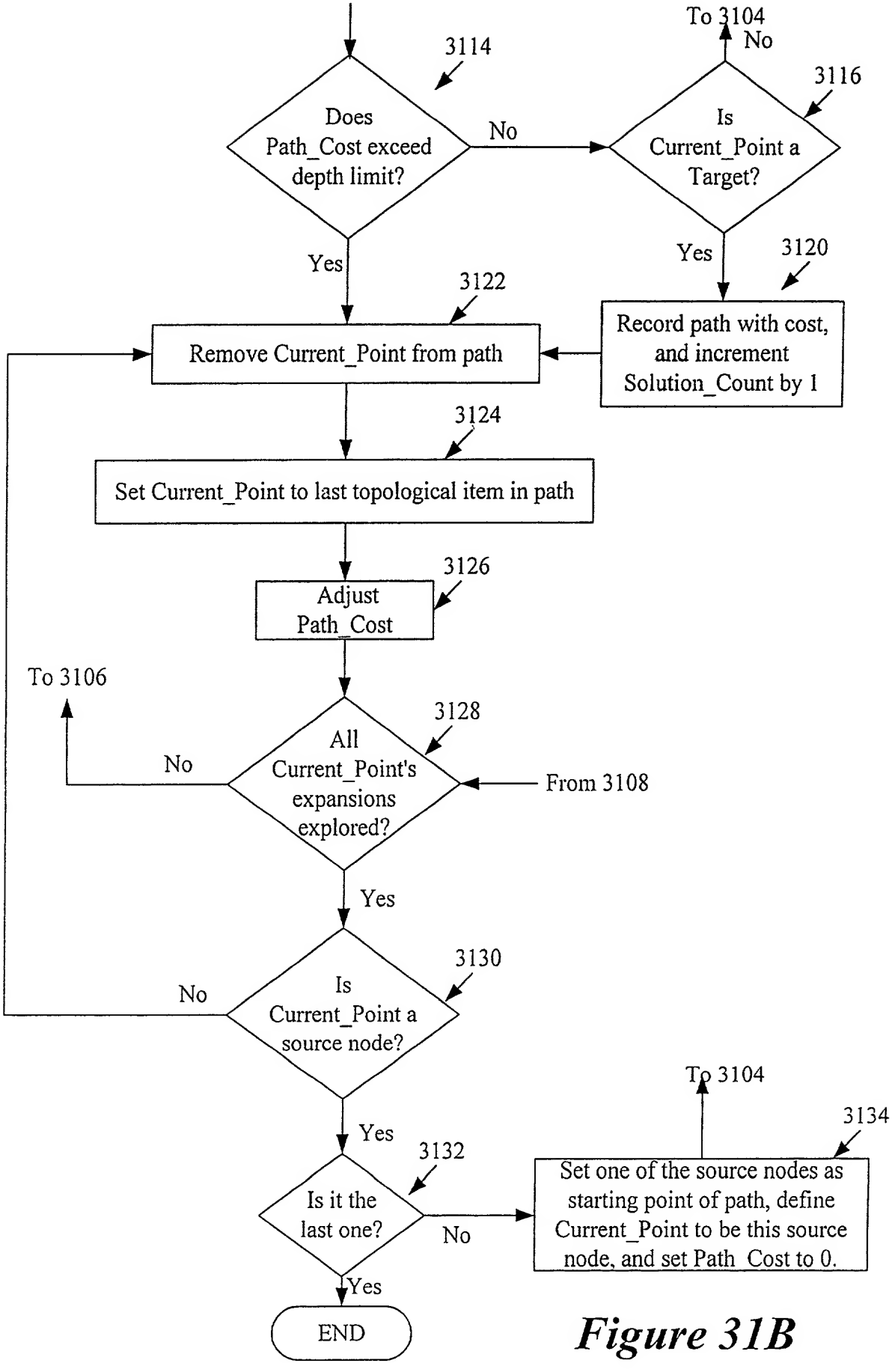

3114
Does Path_Cost exceed depth limit?
No
3116
Is Current_Point a Target?
To 3104
No
Yes
3120
Record path with cost, and increment Solution_Count by 1
Yes
3122
Remove Current_Point from path
3124
Set Current_Point to last topological item in path
3126
Adjust Path_Cost
To 3106
3128
All Current_Point's expansions explored?
No
From 3108
Yes
3130
Is Current_Point a source node?
No
Yes
3132
Is it the last one?
No
To 3104
3134
Set one of the source nodes as starting point of path, define Current_Point to be this source node, and set Path_Cost to 0.
Yes
END

Figure 31B

20020"5609380T

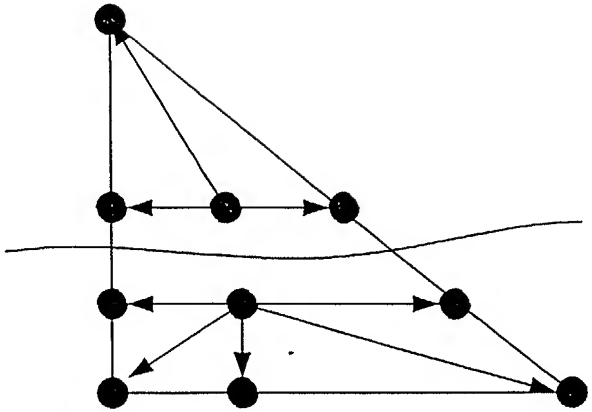


Figure 35

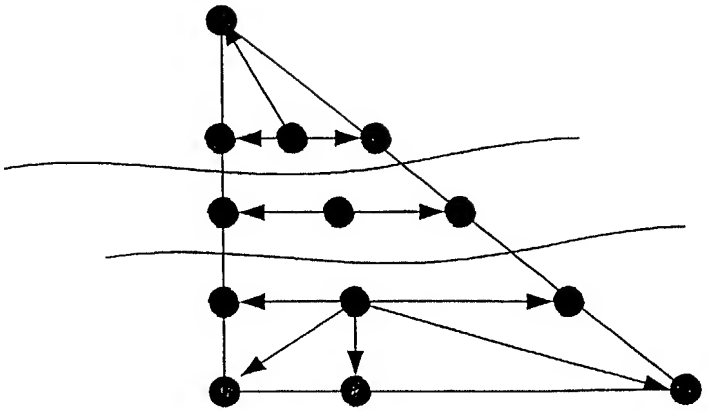


Figure 36

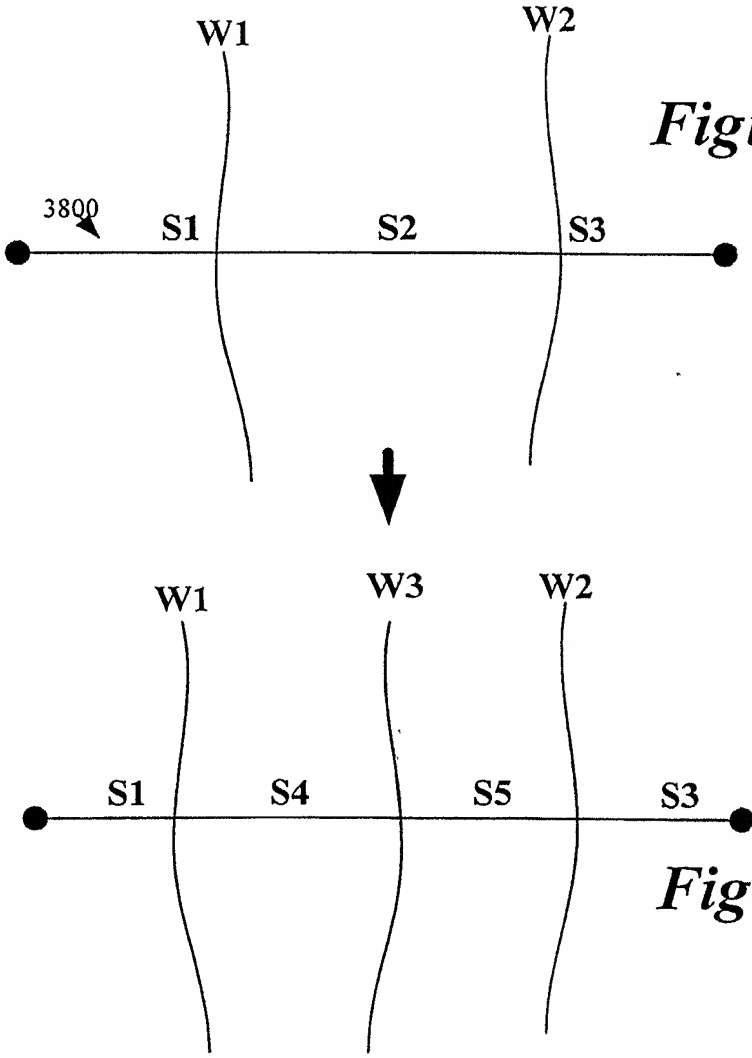


Figure 38A

Figure 38B

From: \ To:	Node	Face Item	Edge Item
Node	• Planarity • Vias	• Vias	• Planarity • Vias • Edge Capacity
Face Item	• Vias	• Vias	• Vias • Edge Capacity
Edge Item	• Planarity • Vias	• Vias	• Planarity • Vias • Edge Capacity

Figure 37

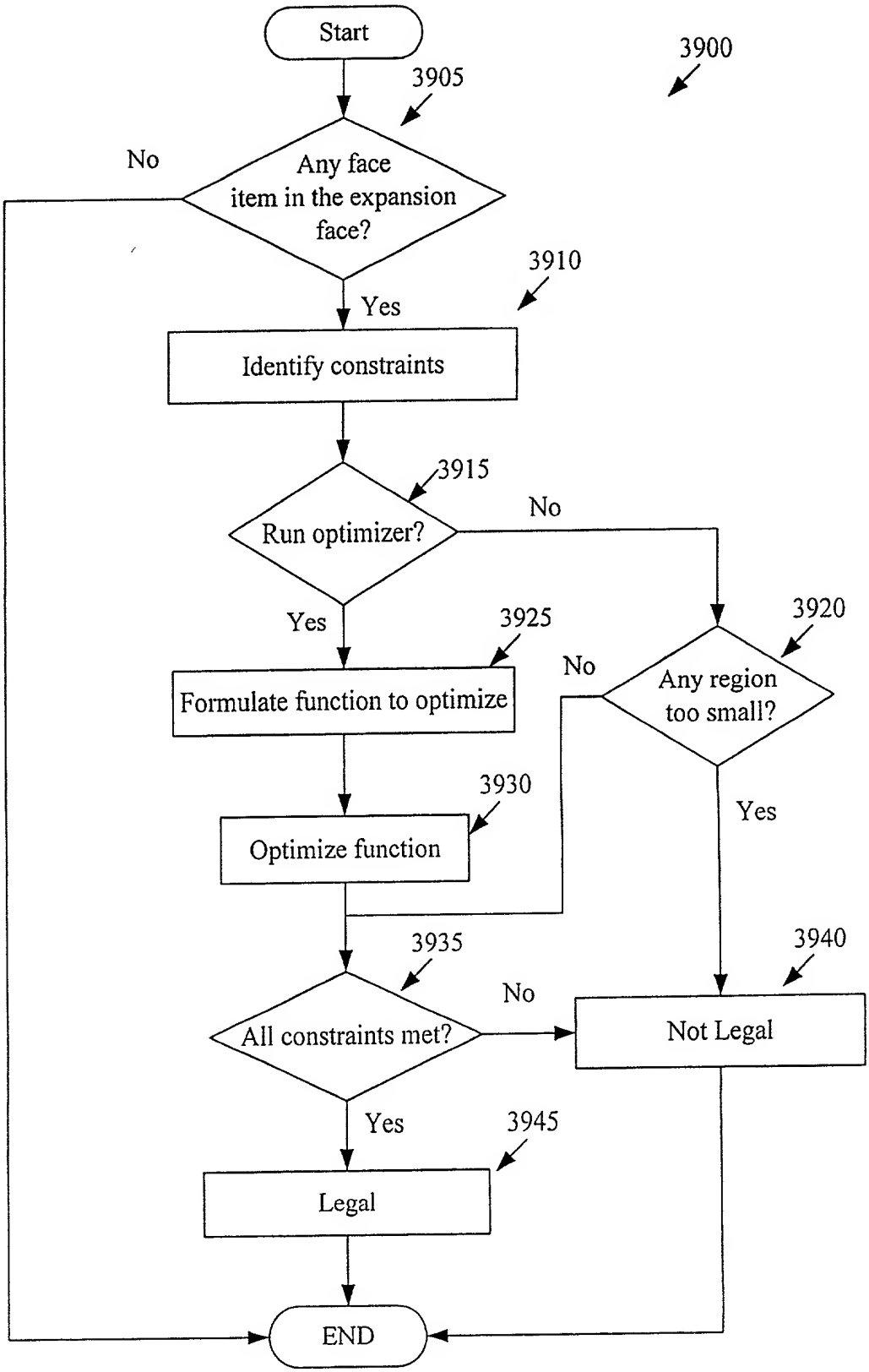

Start
3900
3905
Any face item in the expansion face?
No
Yes
3910
Identify constraints
3915
Run optimizer?
No
Yes
3925
Formulate function to optimize
3920
Any region too small?
No
Yes
3930
Optimize function
3935
All constraints met?
No
3940
Not Legal
Yes
3945
Legal
END

Figure 39A

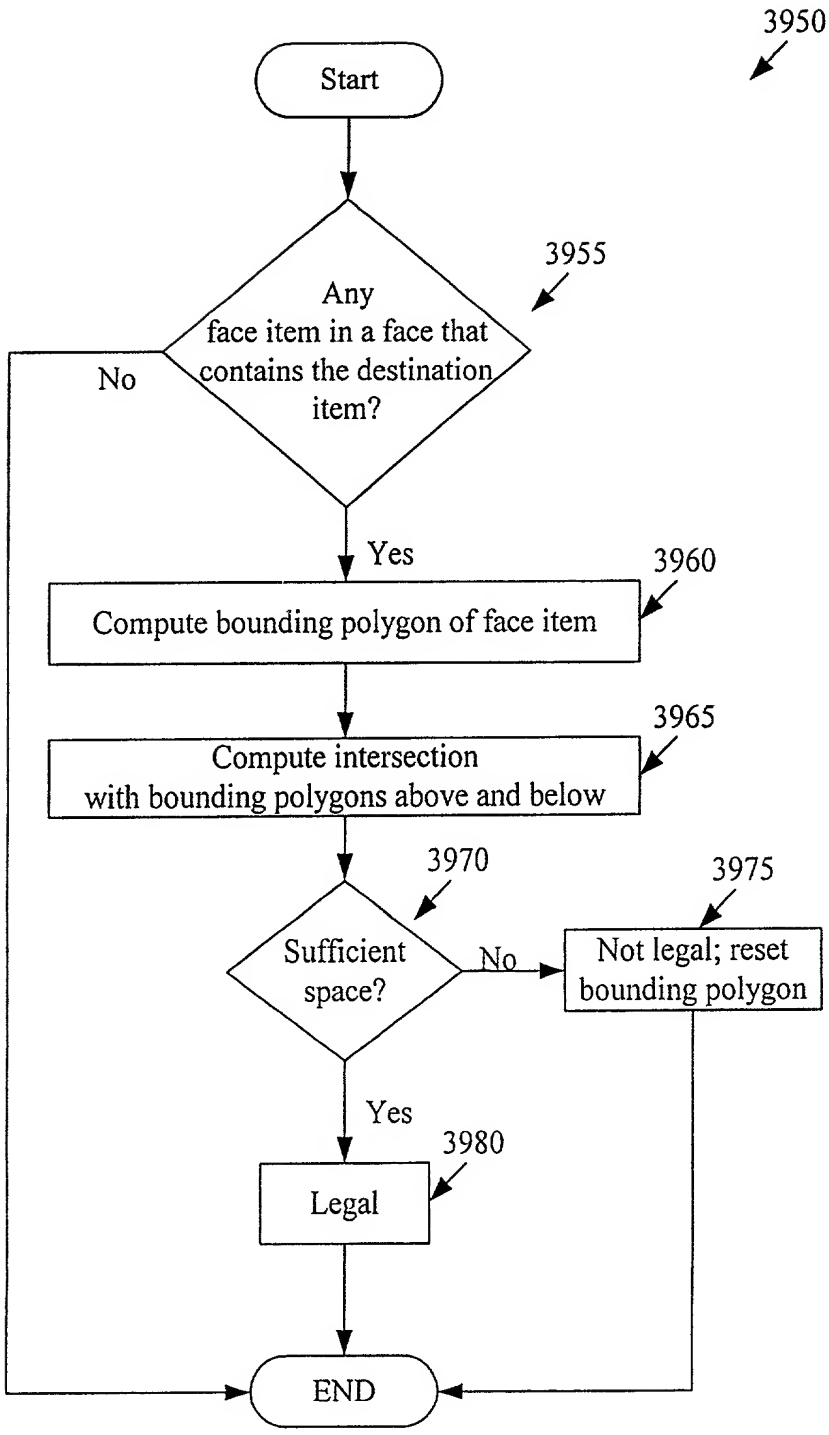

3950
Start
3955
Any face item in a face that contains the destination item?
No
Yes
3960
Compute bounding polygon of face item
3965
Compute intersection with bounding polygons above and below
3970
Sufficient space?
No
3975
Not legal; reset bounding polygon
Yes
3980
Legal
END

Figure 39B

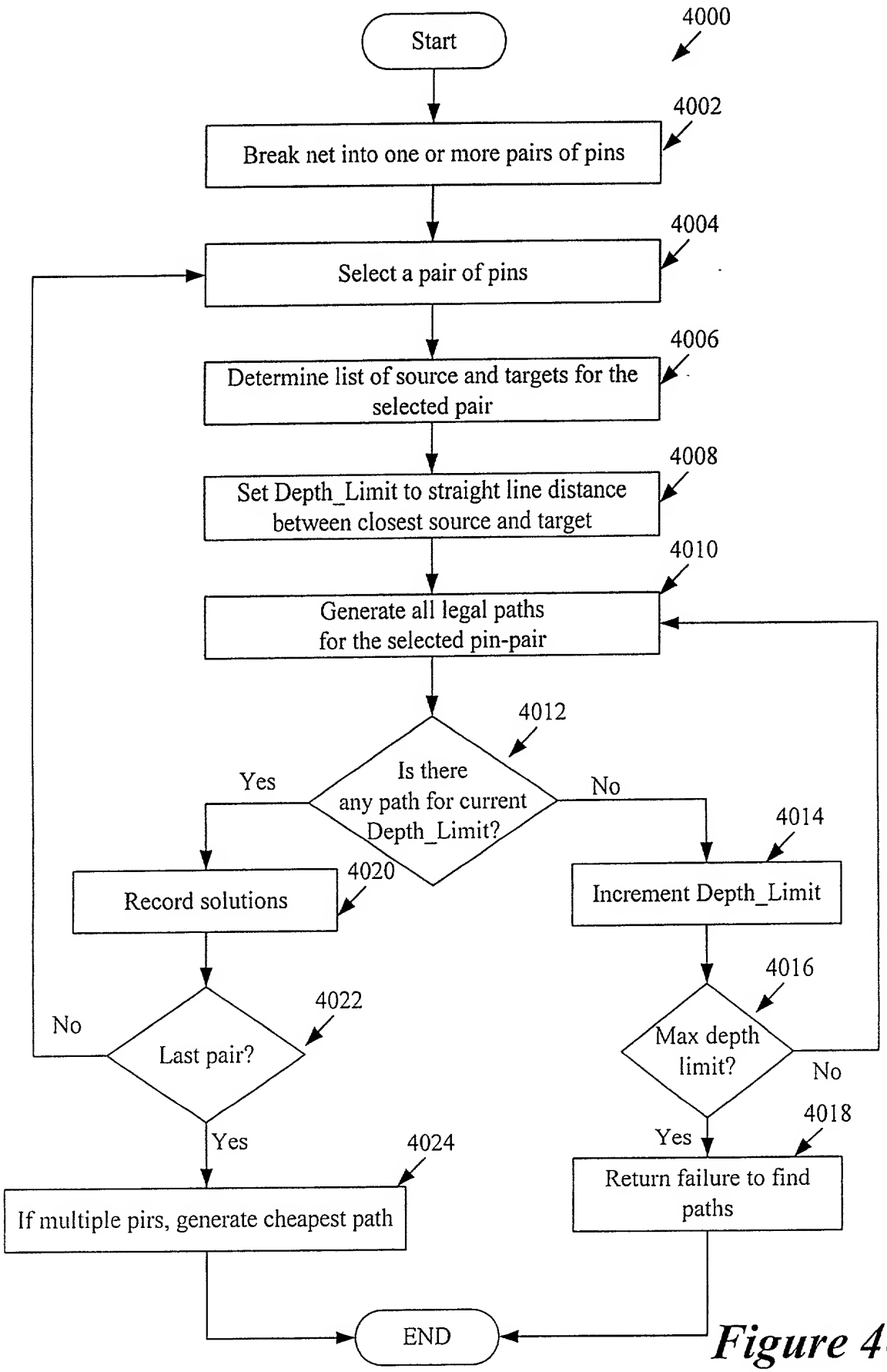

Start
4000
Break net into one or more pairs of pins
4002
Select a pair of pins
4004
Determine list of source and targets for the selected pair
4006
Set Depth_Limit to straight line distance between closest source and target
4008
Generate all legal paths for the selected pin-pair
4010
Is there any path for current Depth_Limit?
4012
Yes
No
Record solutions
4020
Increment Depth_Limit
4014
Last pair?
4022
No
Yes
Max depth limit?
4016
No
Yes
Return failure to find paths
4018
If multiple pirs, generate cheapest path
4024
END

Figure 40

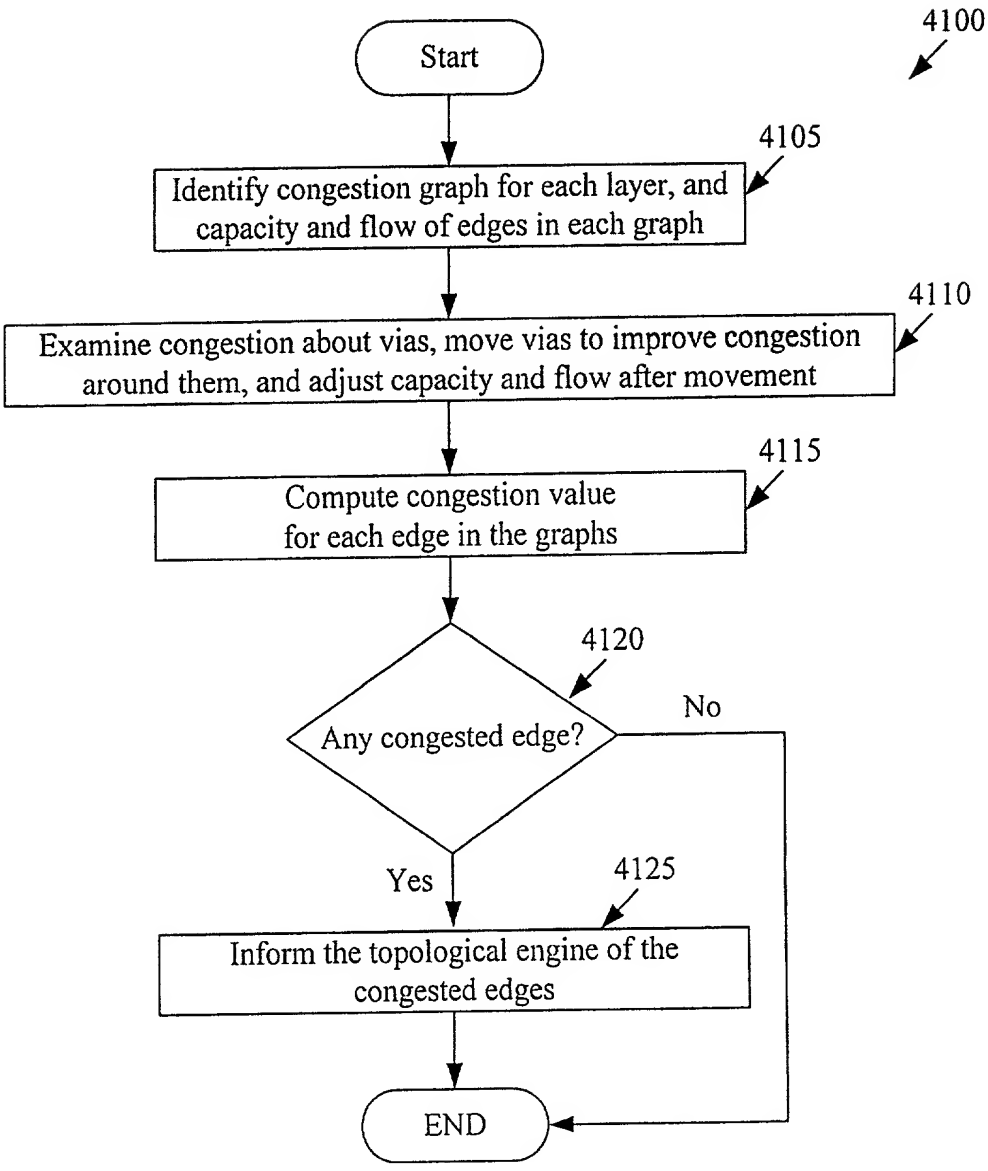

4100
Start
4105
Identify congestion graph for each layer, and capacity and flow of edges in each graph
4110
Examine congestion about vias, move vias to improve congestion around them, and adjust capacity and flow after movement
4115
Compute congestion value for each edge in the graphs
4120
Any congested edge?
No
Yes
4125
Inform the topological engine of the congested edges
END

Figure 41

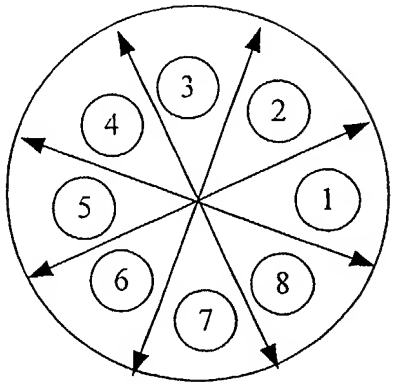


Figure 42

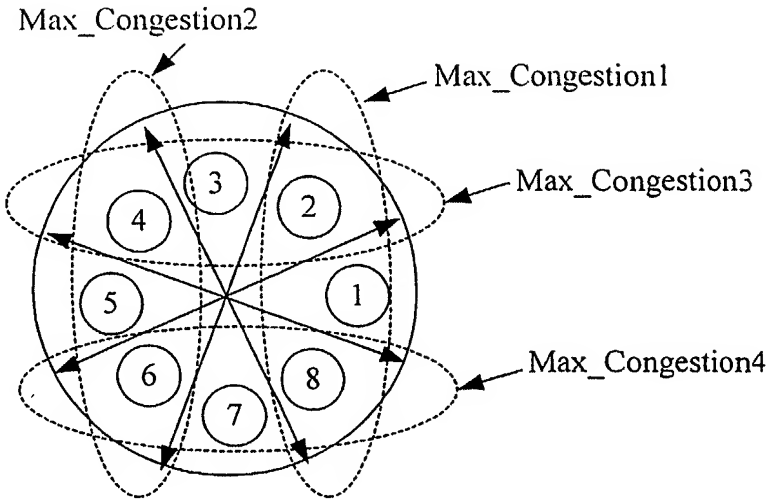


Figure 44

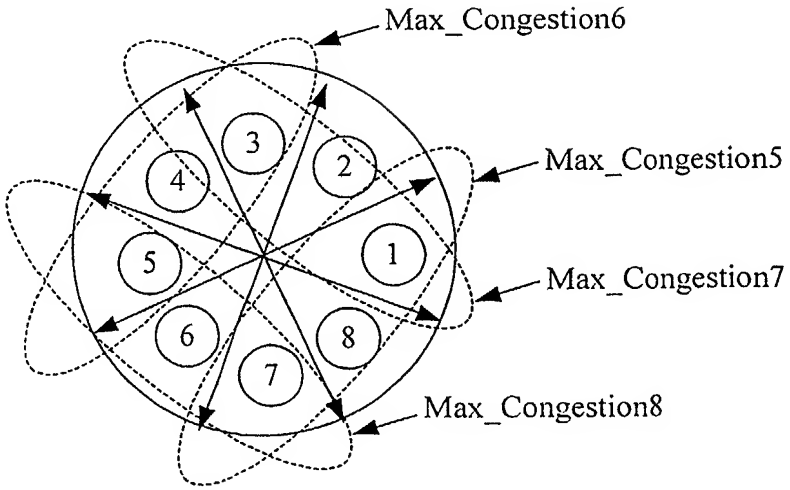


Figure 45

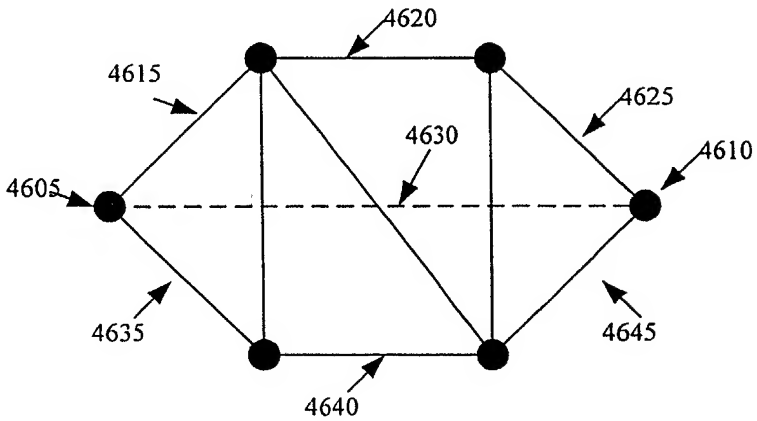


Figure 46

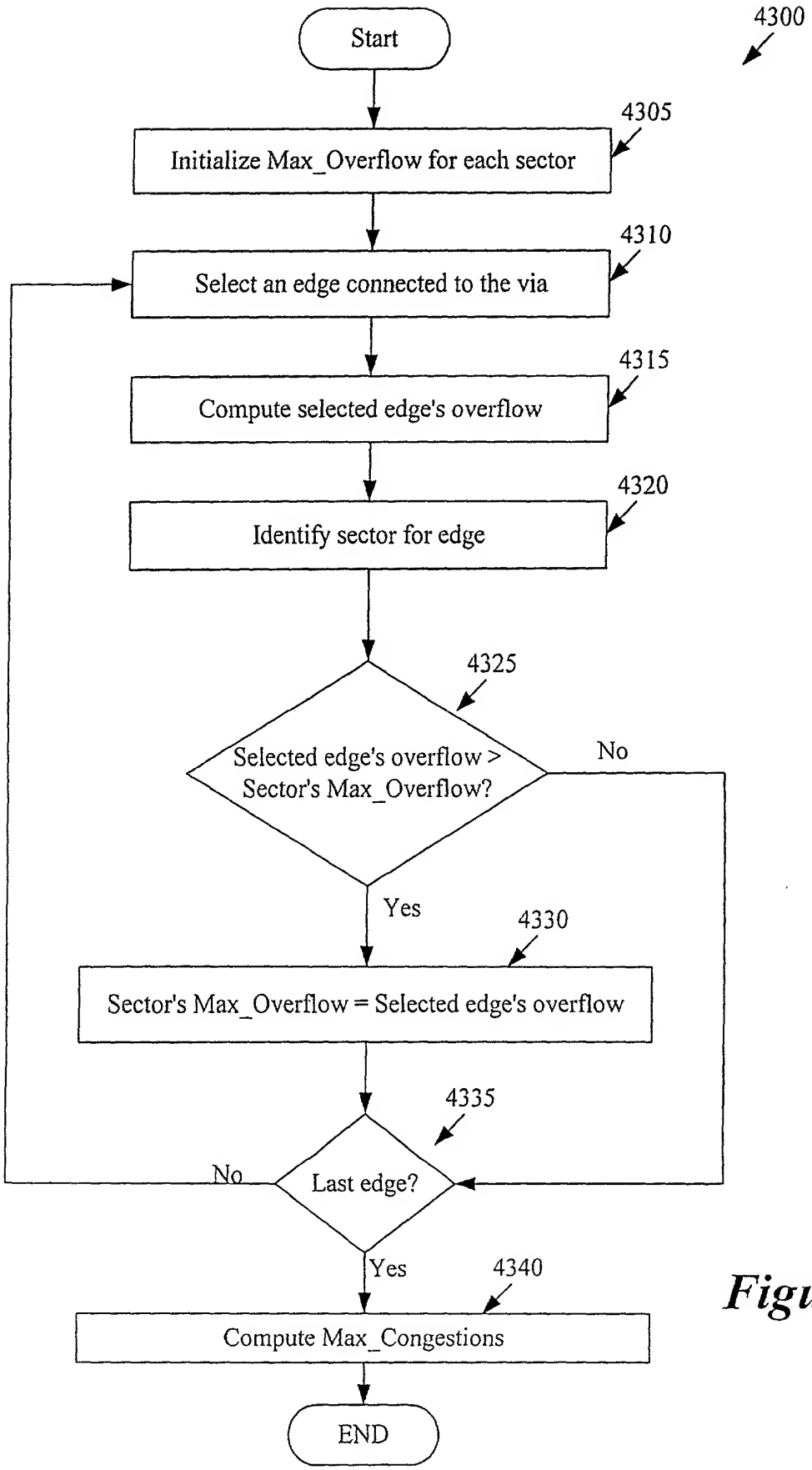

4300
Start
4305
Initialize Max_Overflow for each sector
4310
Select an edge connected to the via
4315
Compute selected edge's overflow
4320
Identify sector for edge
4325
Selected edge's overflow > Sector's Max_Overflow?
No
Yes
4330
Sector's Max_Overflow = Selected edge's overflow
4335
No
Last edge?
Yes
4340
Compute Max_Congestions
END

Figure 43

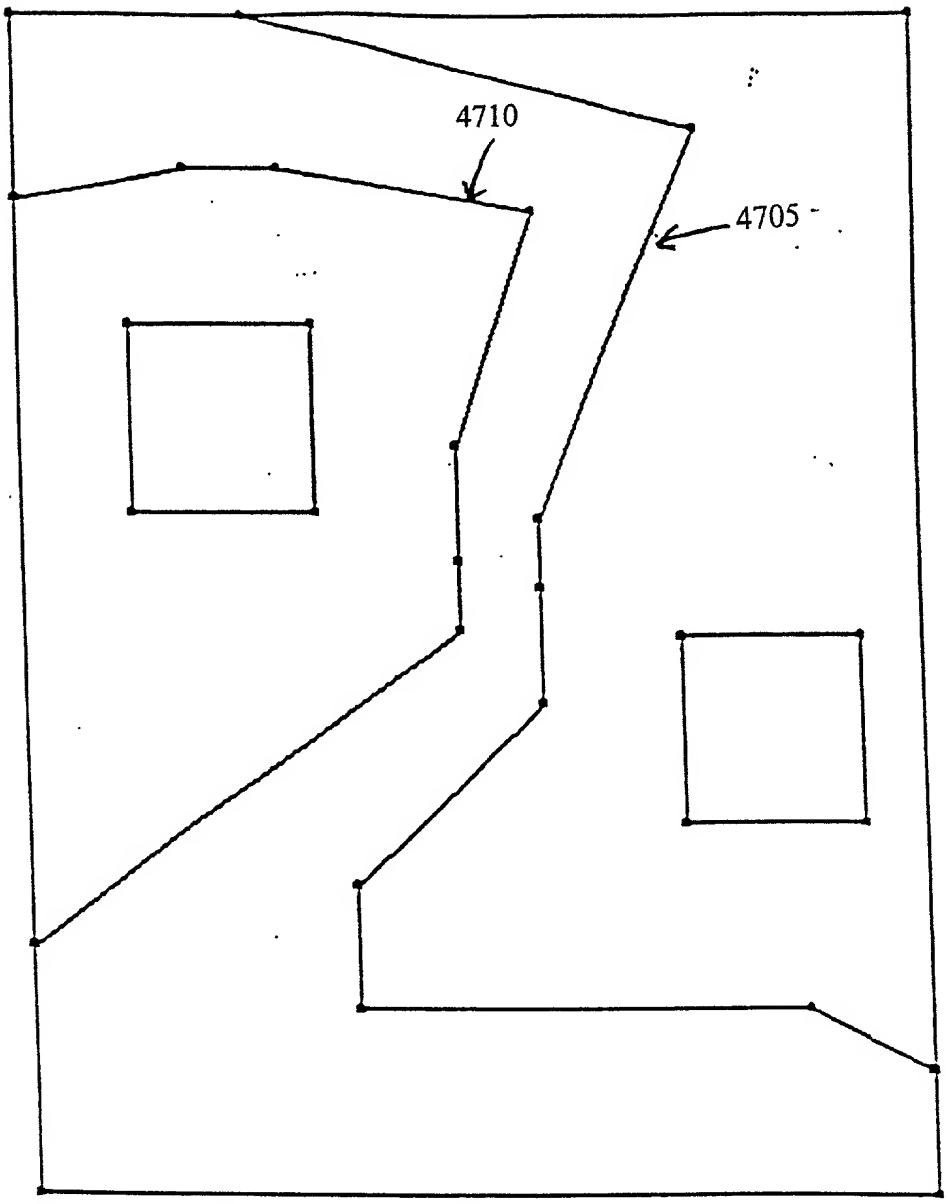


FIGURE 47

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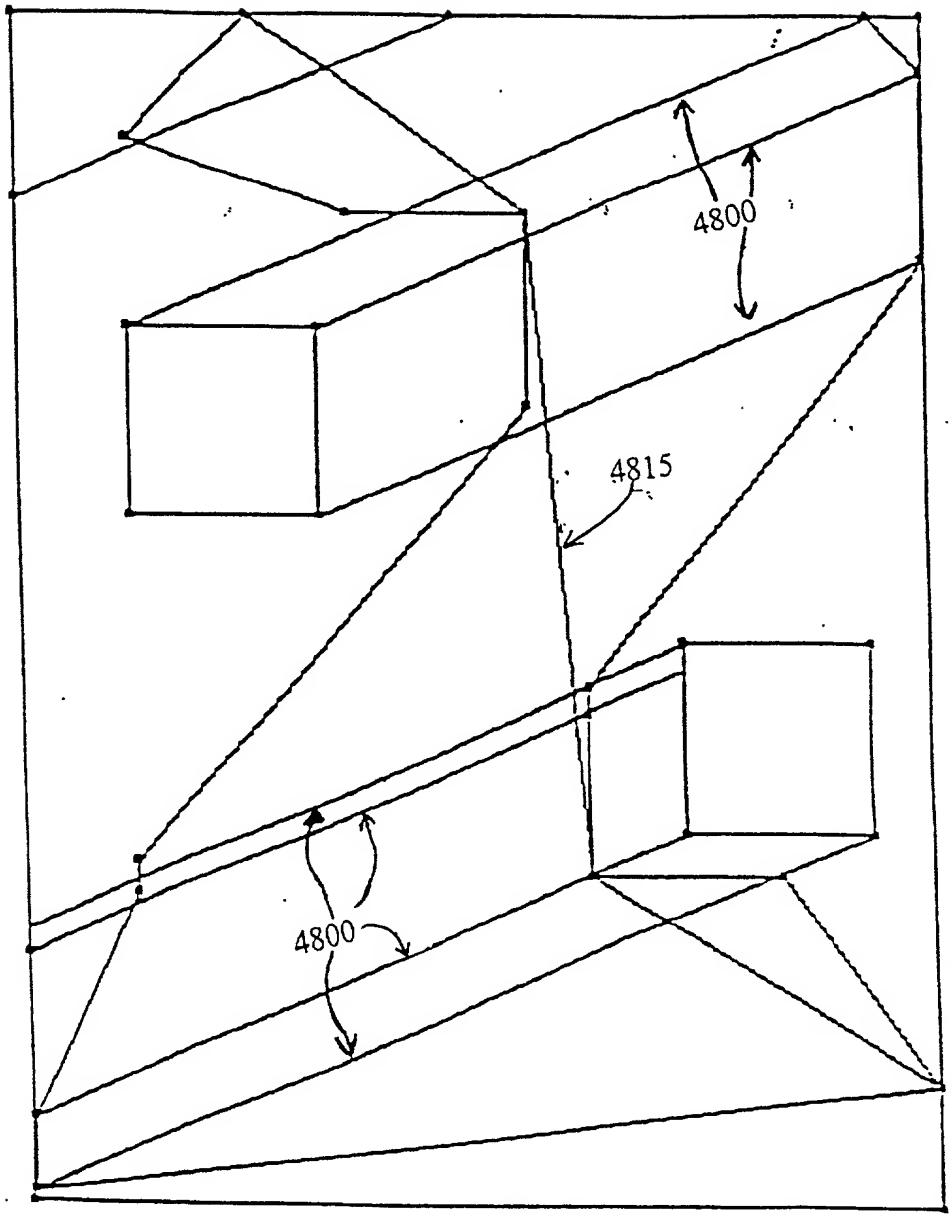

FIGURE 48A

20150705.5595001

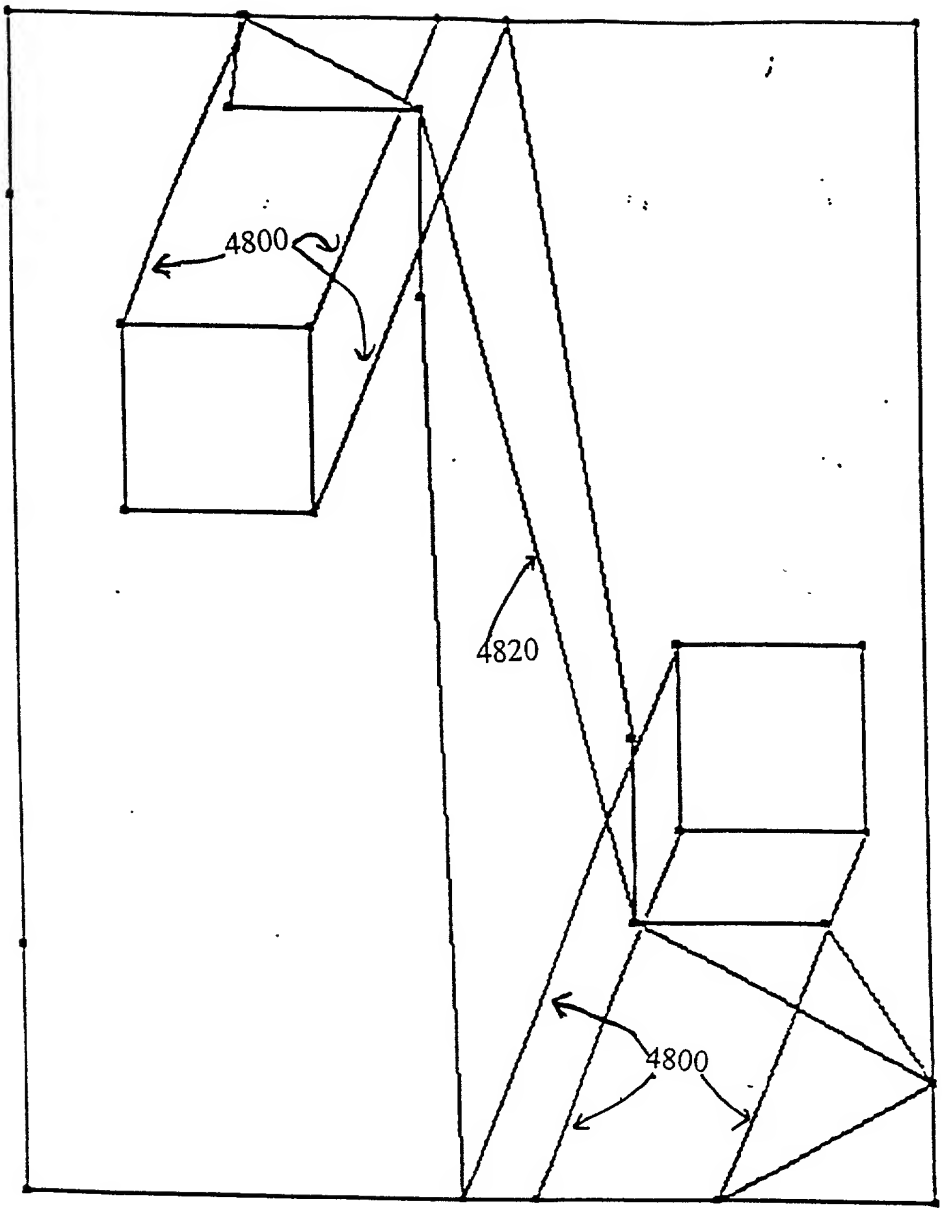

FIGURE 48B

2015-05-29 15:39:07

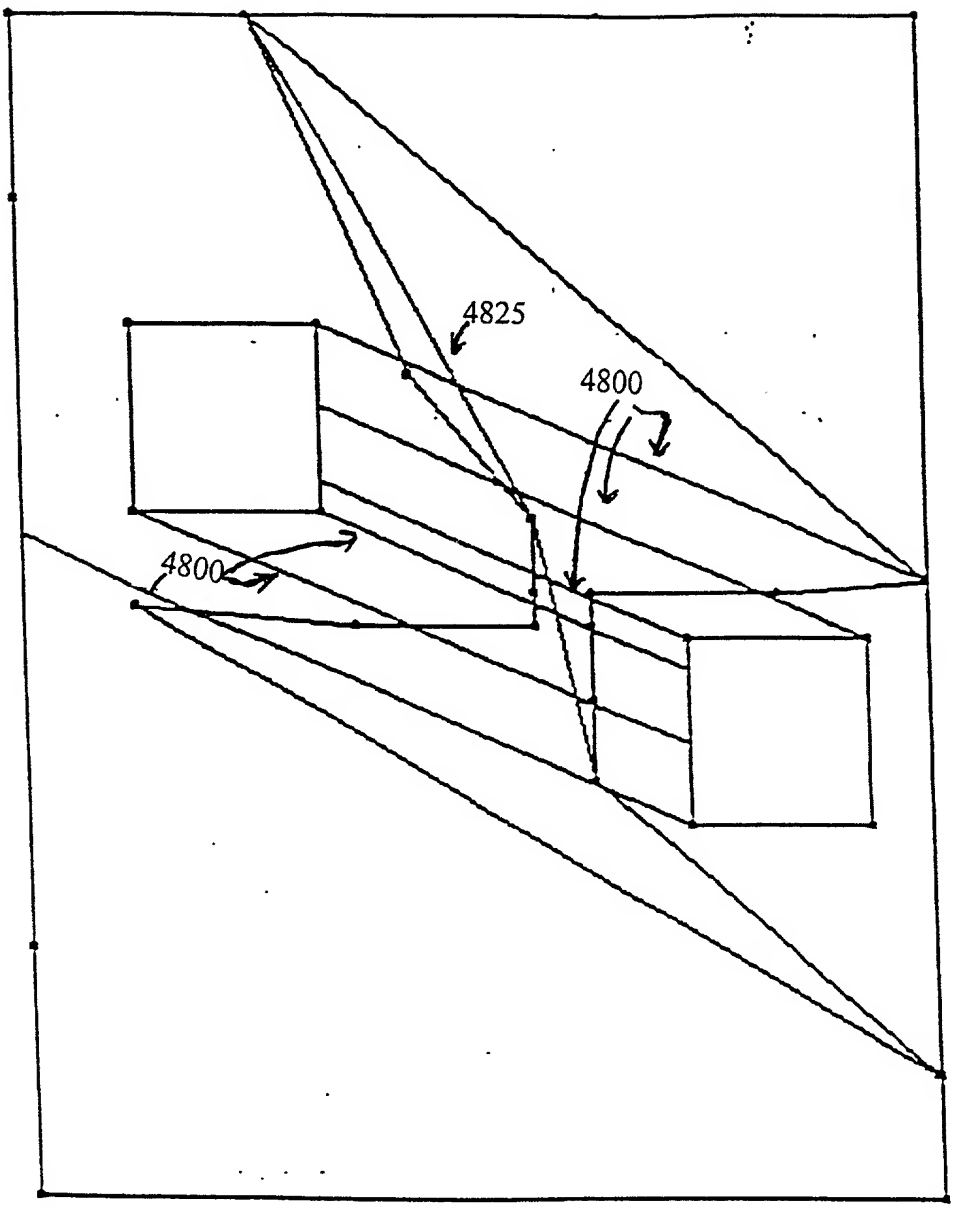

FIGURE 48C

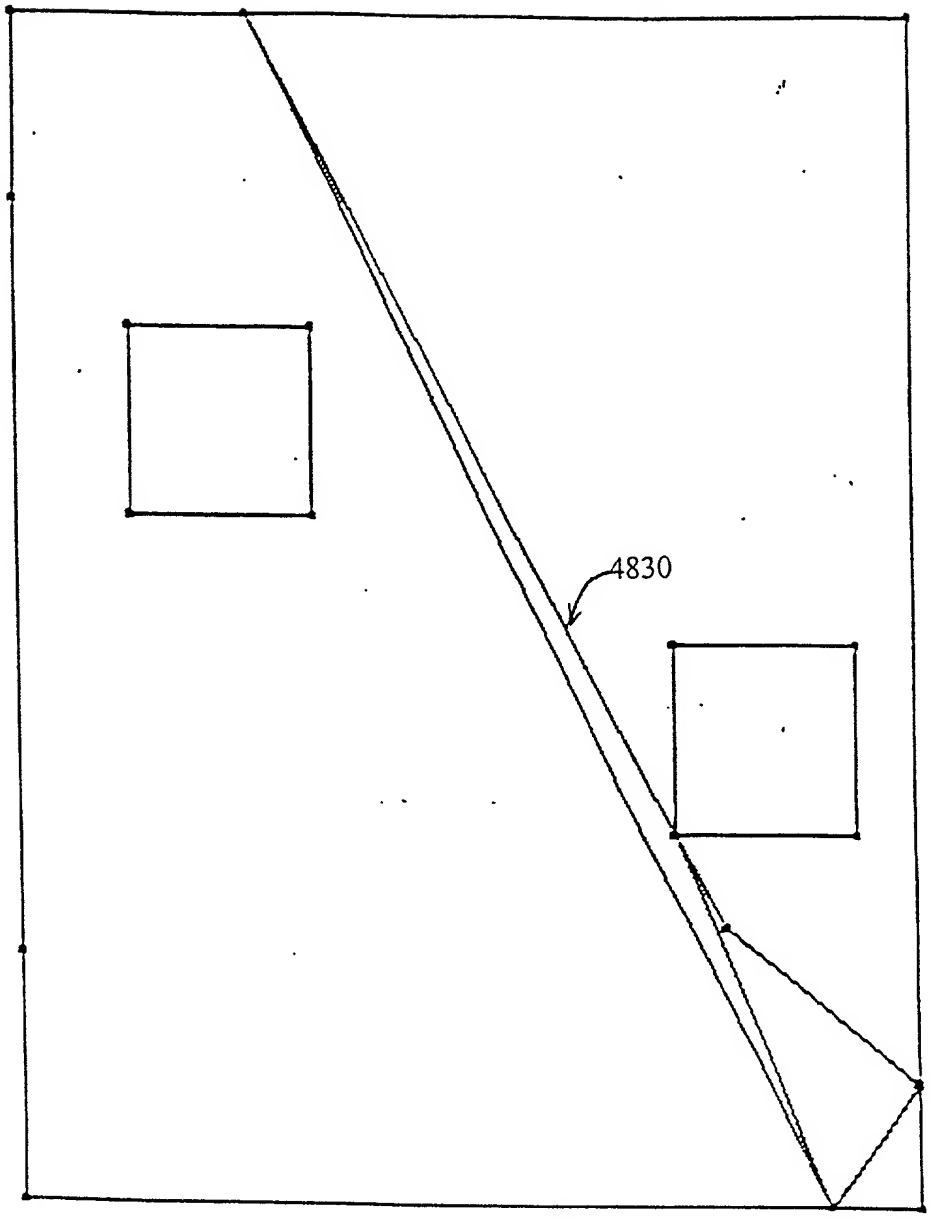

FIGURE 48D

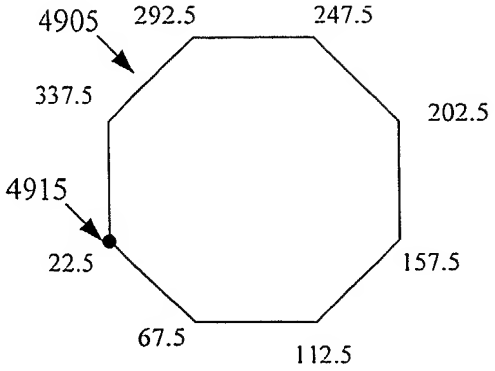

Figure 49A

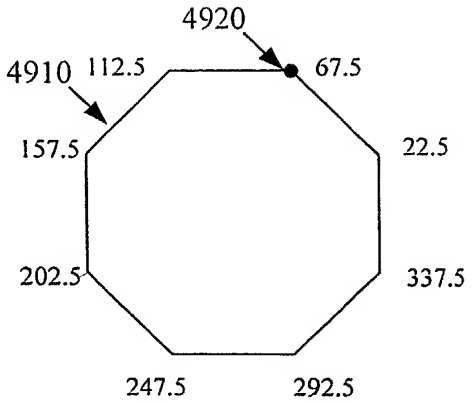

Figure 49B

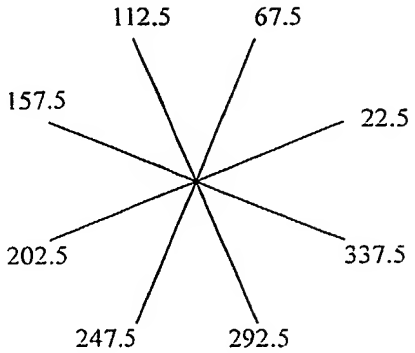

Figure 49C

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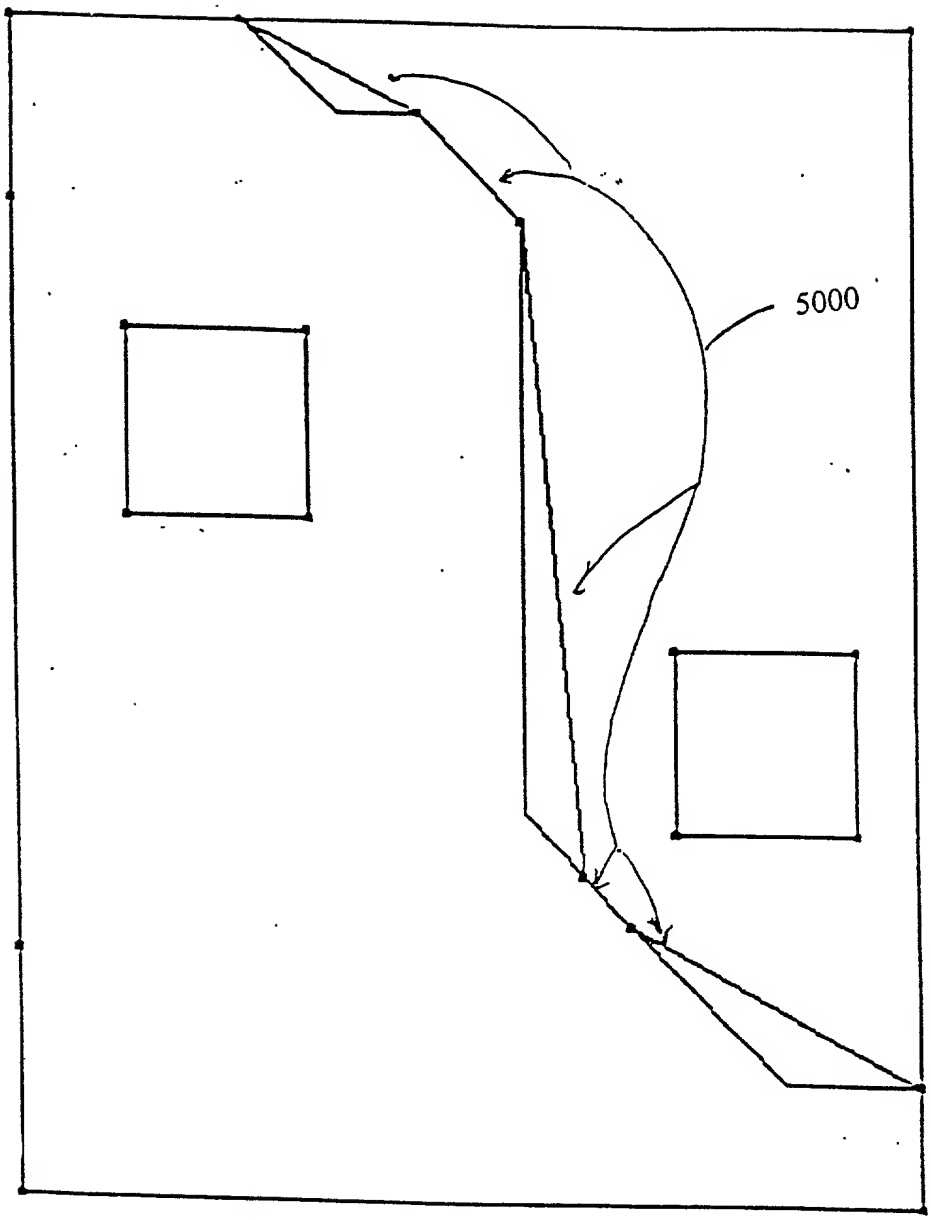

FIGURE 50

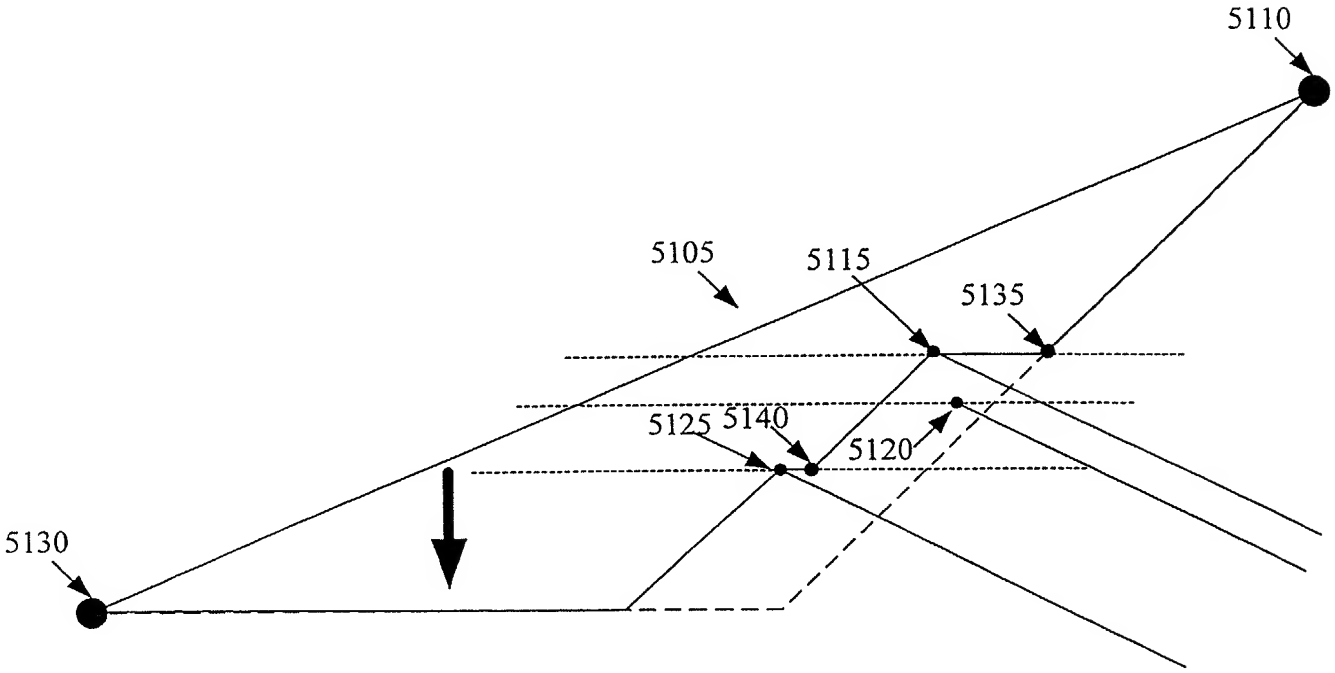

Figure 51

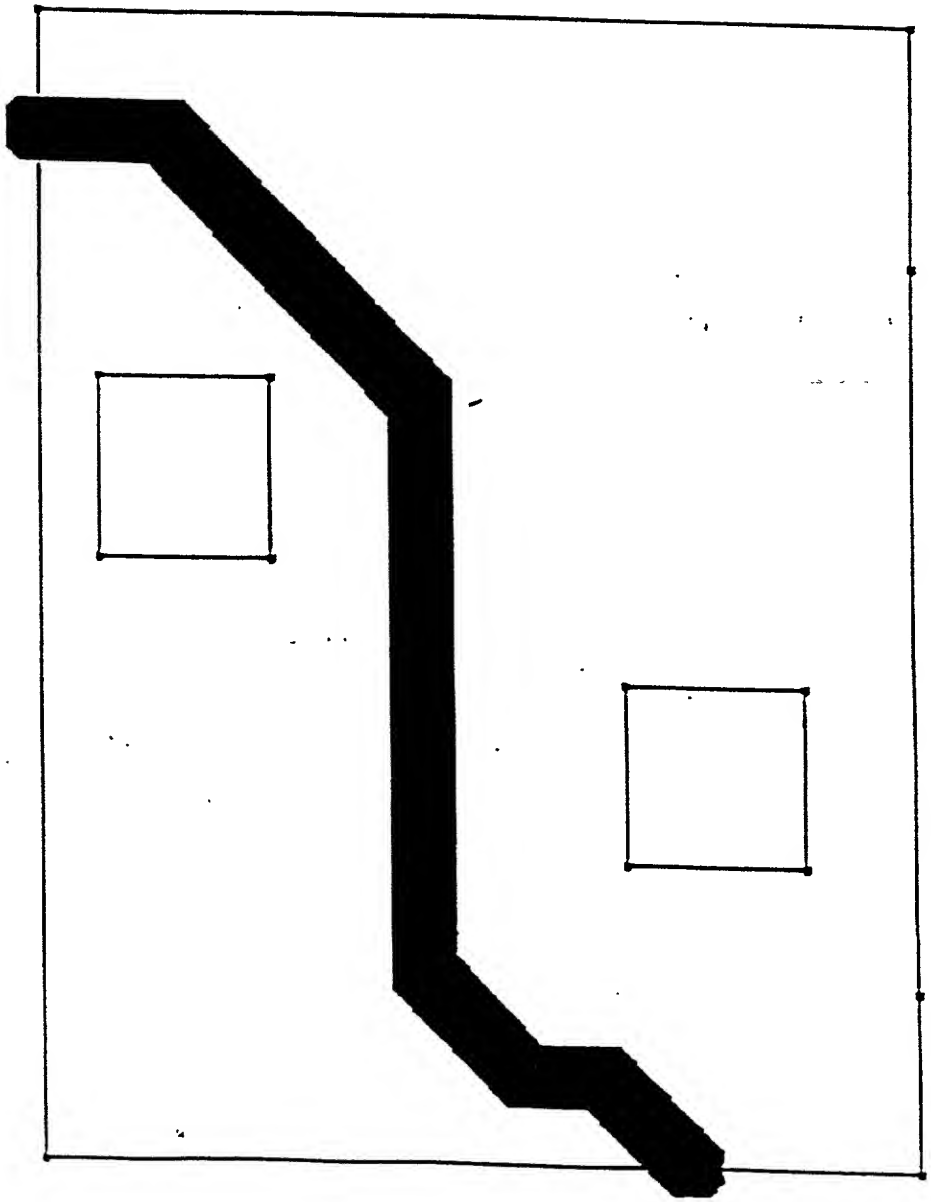

FIGURE 52

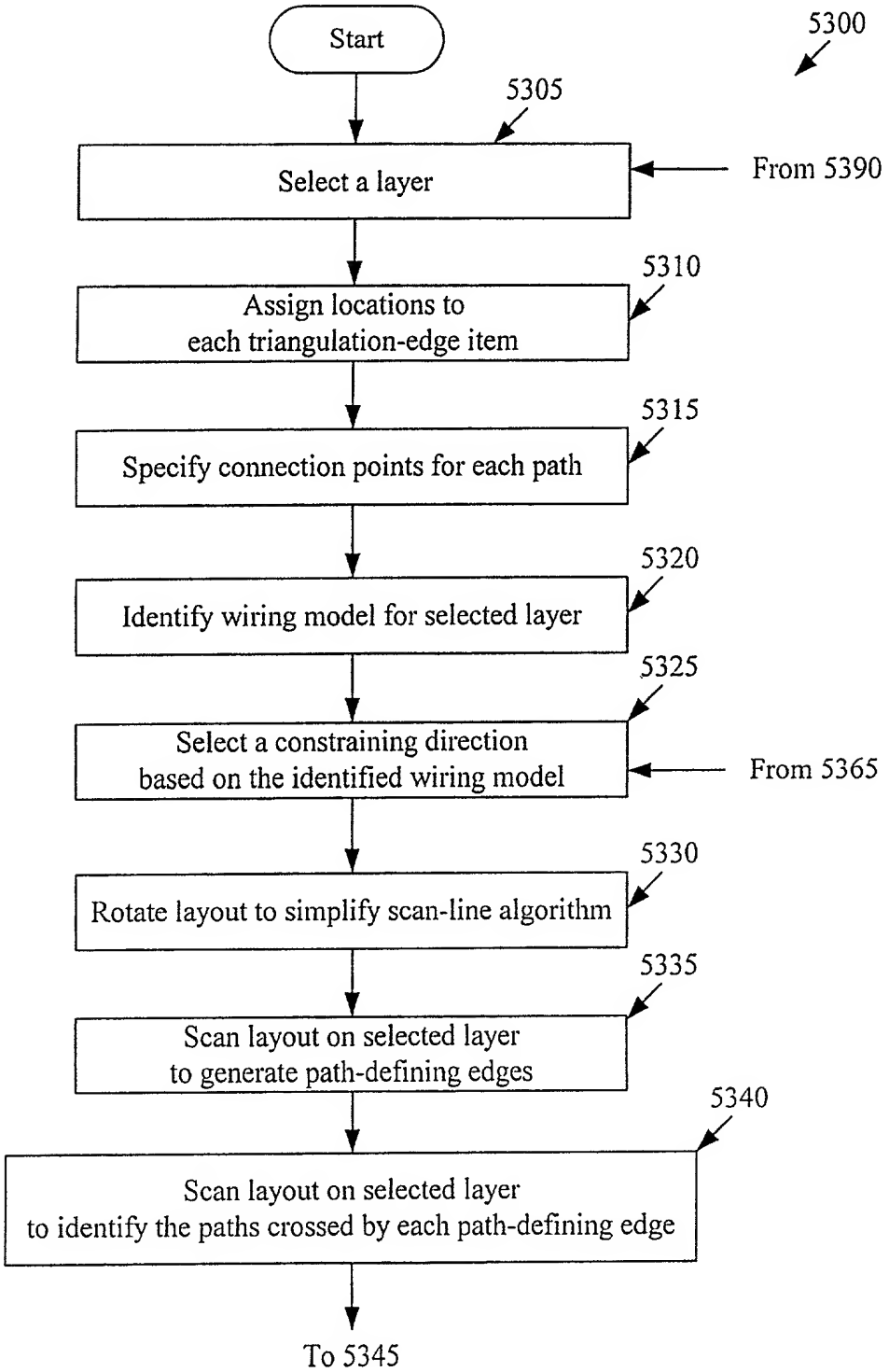

Figure 53

Figure 53: *Figure 53A* / *Figure 53B*

20120326-5550007

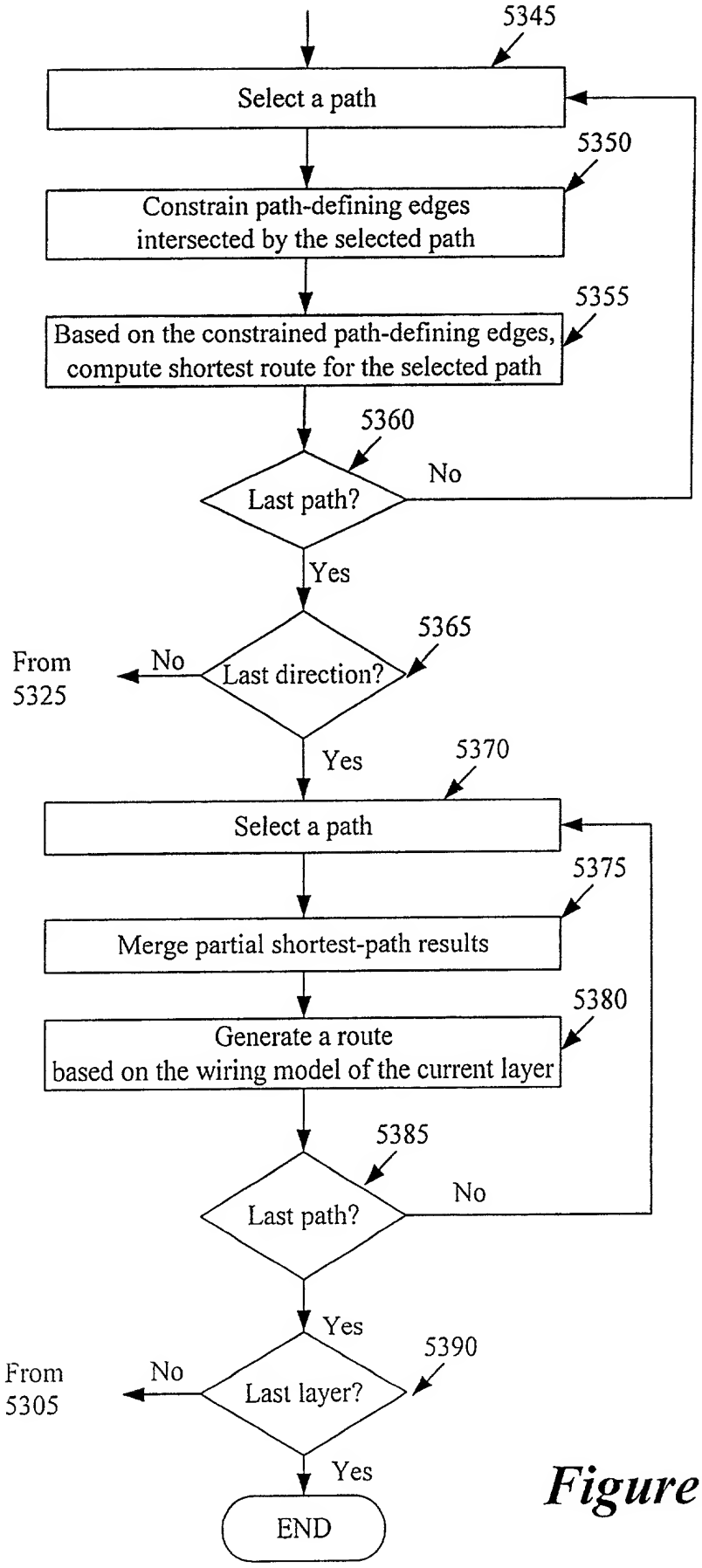

Figure 53B

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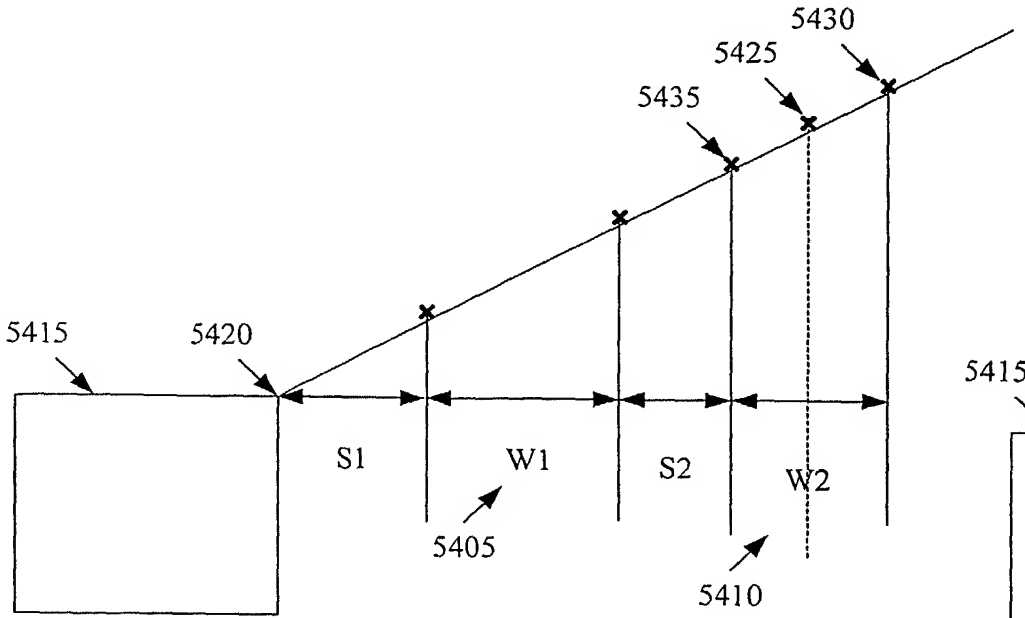

Figure 54

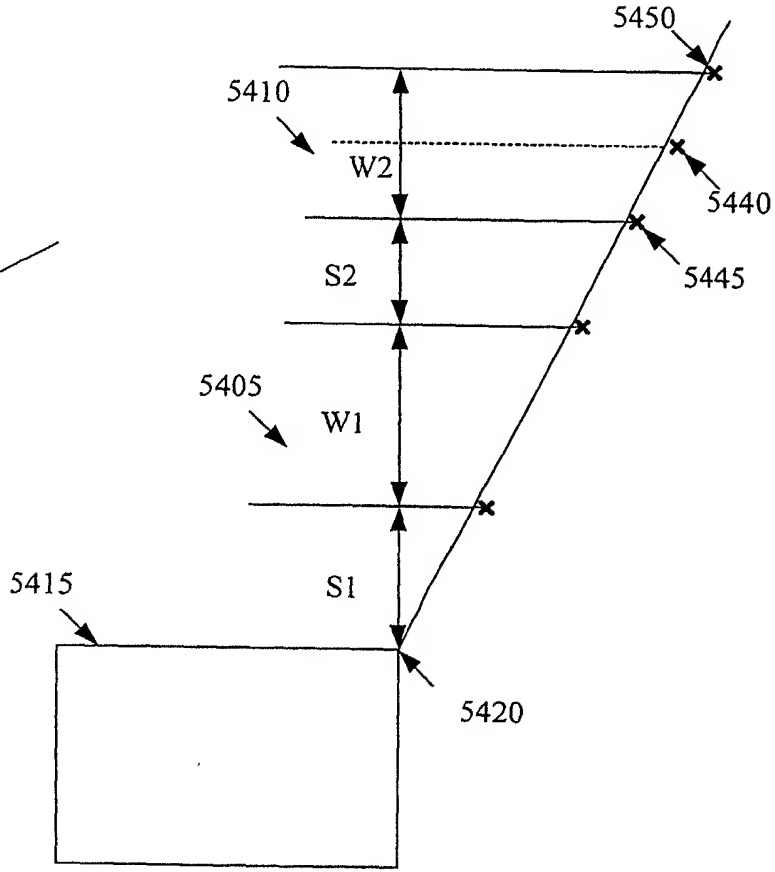

Figure 55

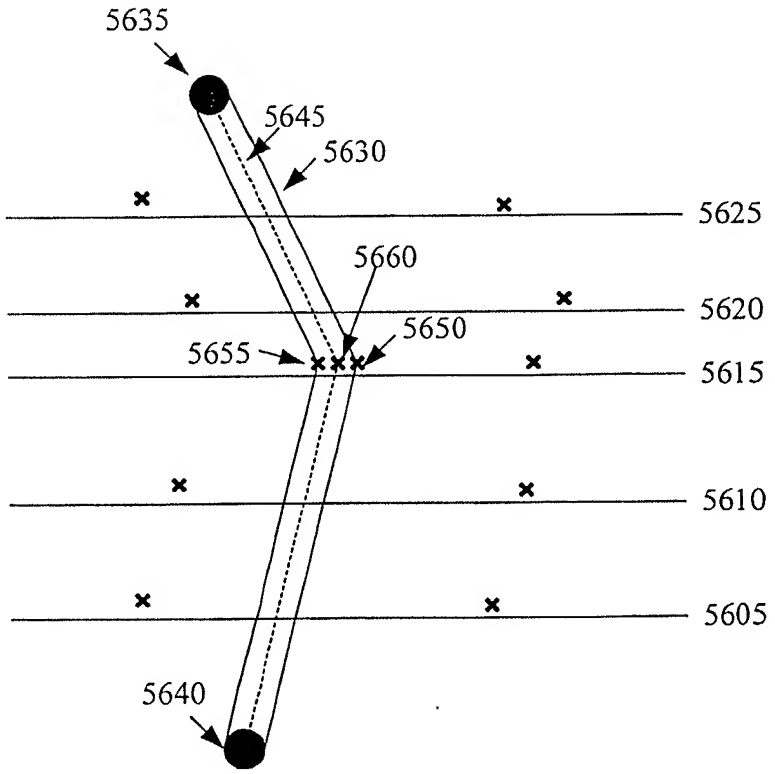

Figure 56

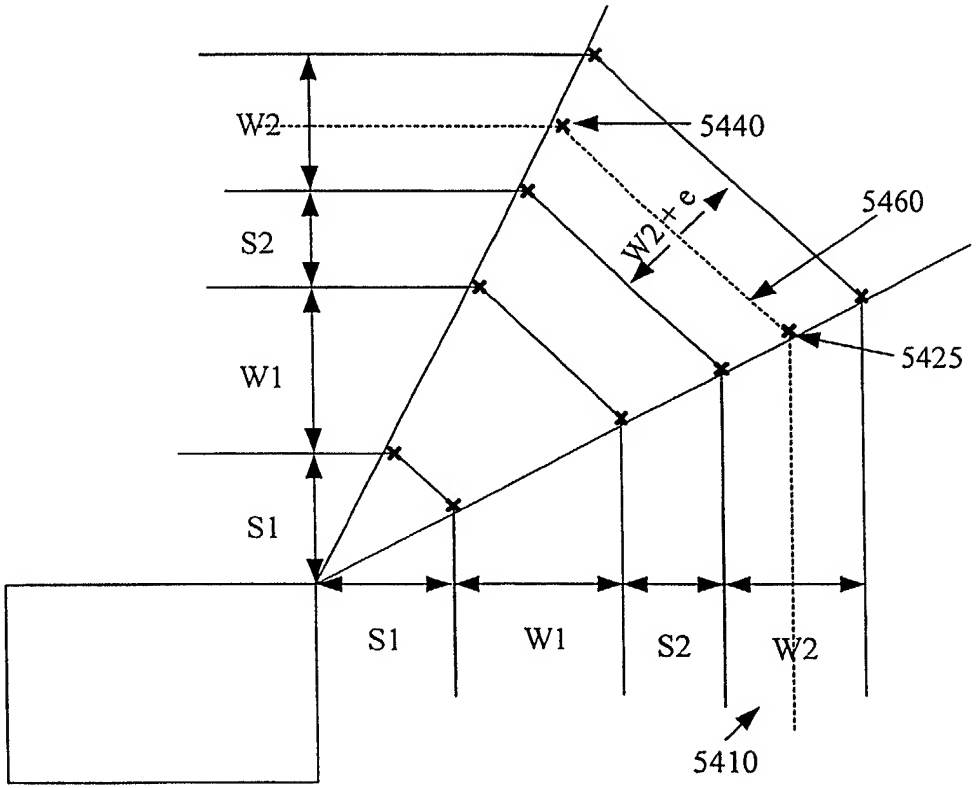

Figure 57

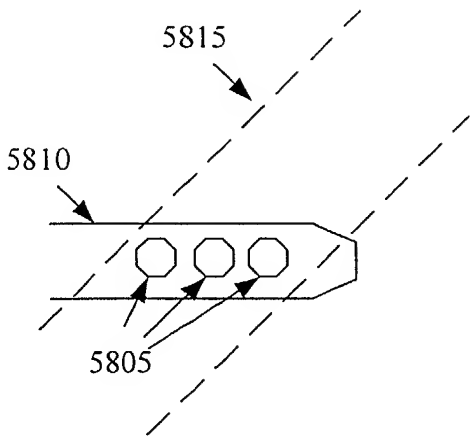

Figure 58

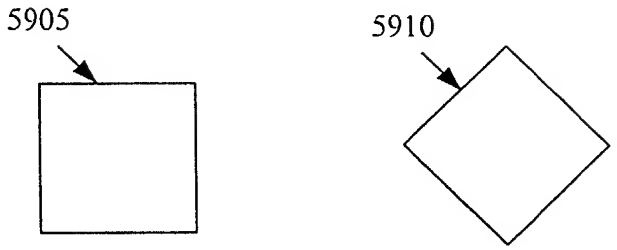

Figure 59

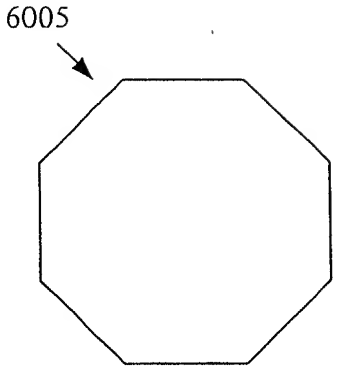

Figure 60

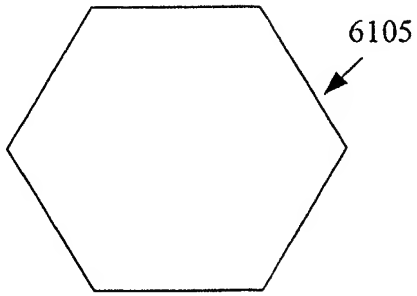

Figure 61

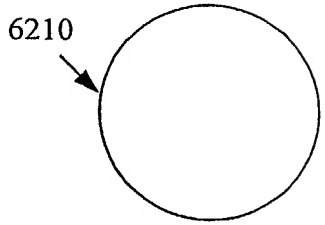

Figure 62

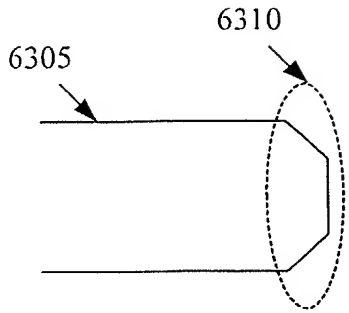

Figure 63

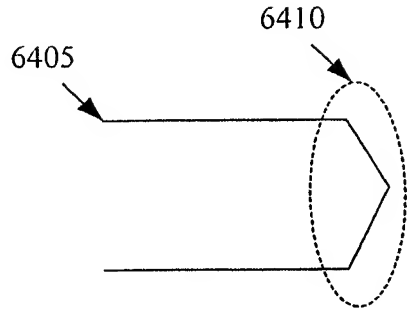

Figure 64

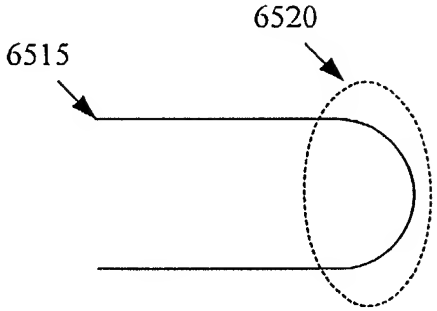

Figure 65

(1) *(2)* *(3)*

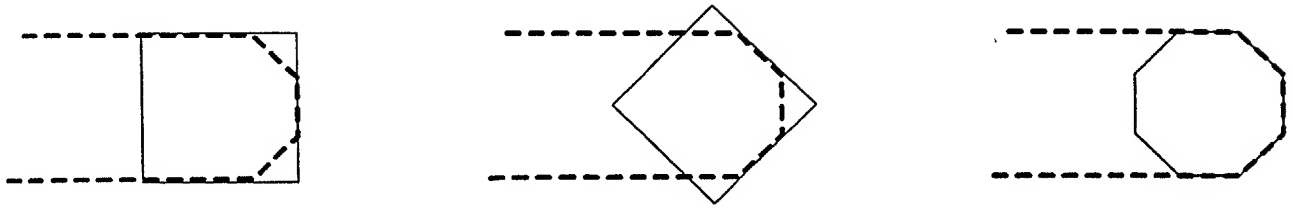

Figure 66

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Figure 67

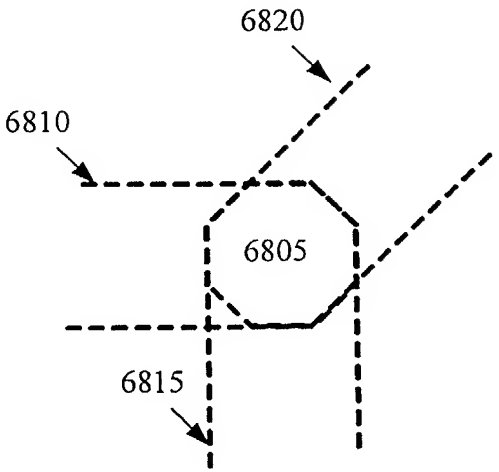

Figure 68

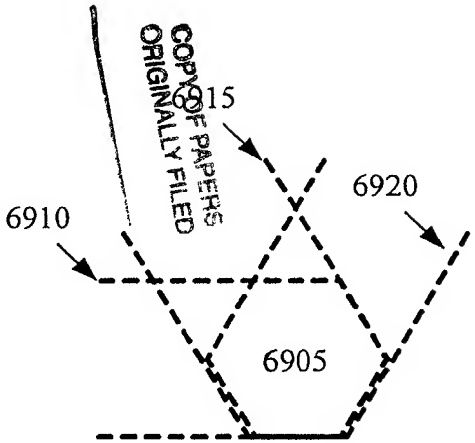

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Figure 69

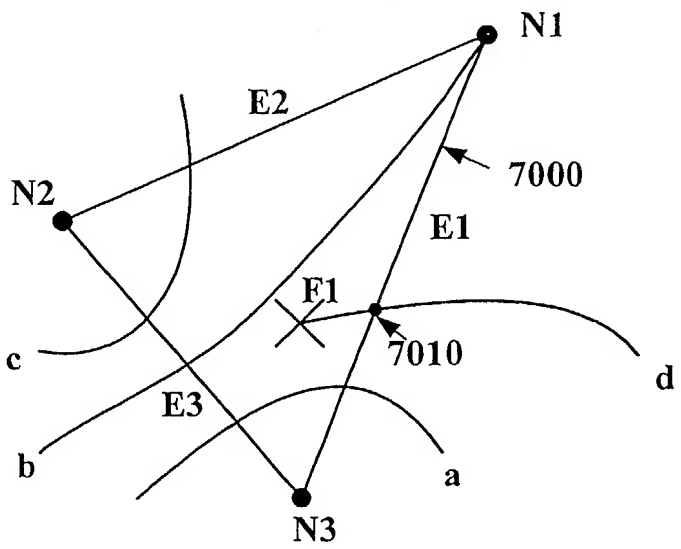


Figure 70

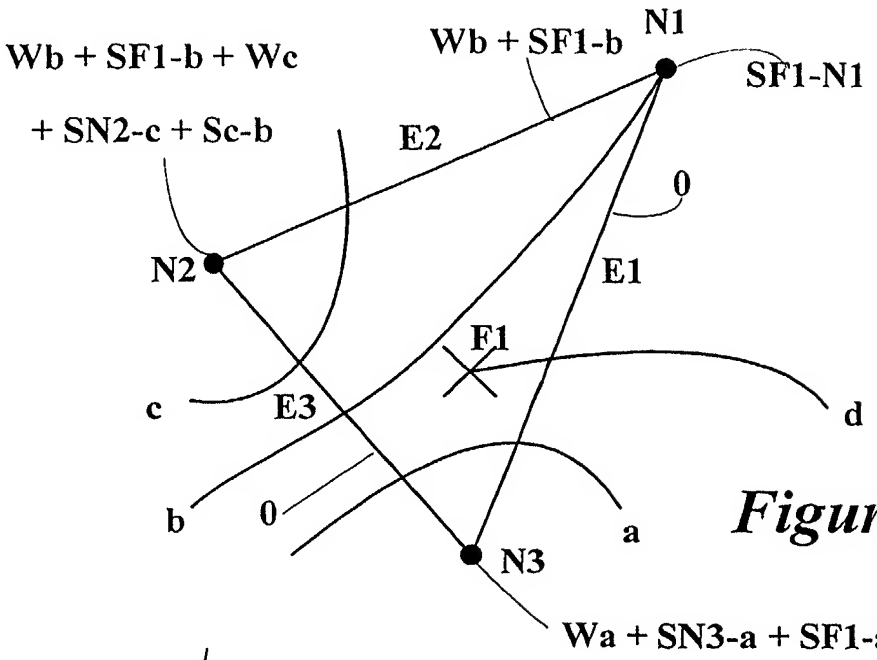


Figure 71

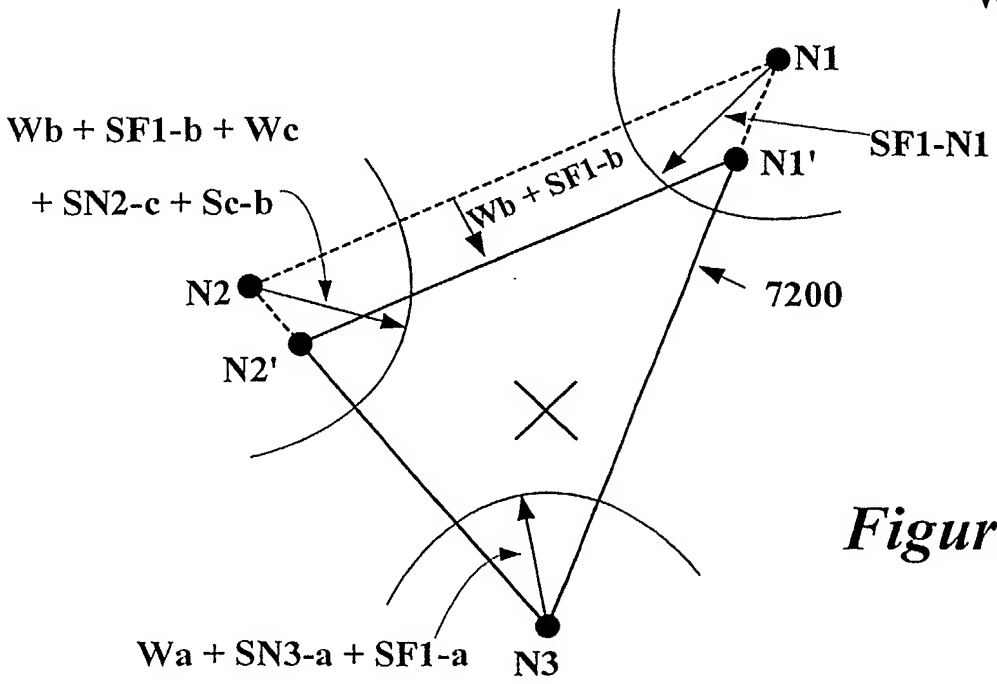
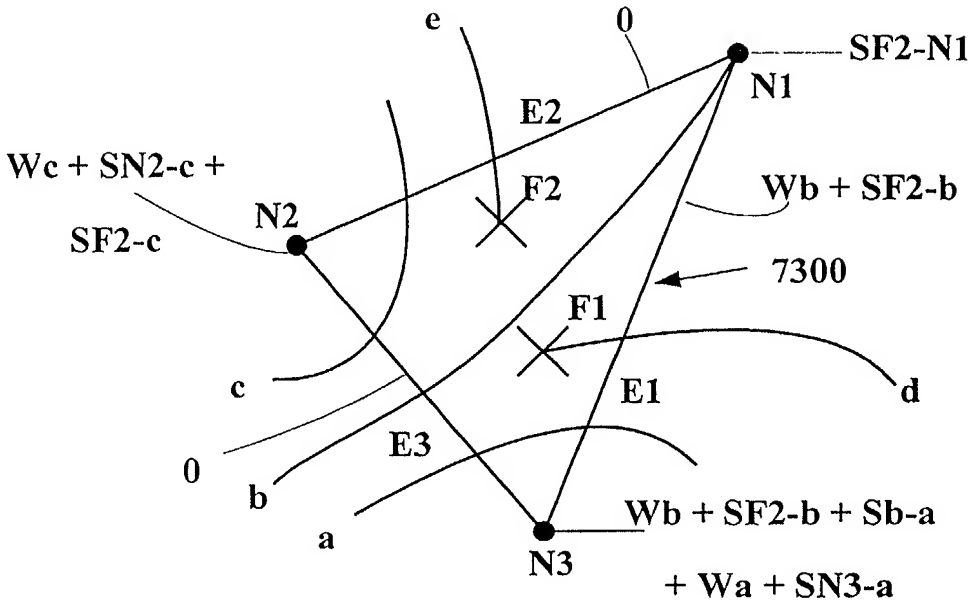


Figure 72



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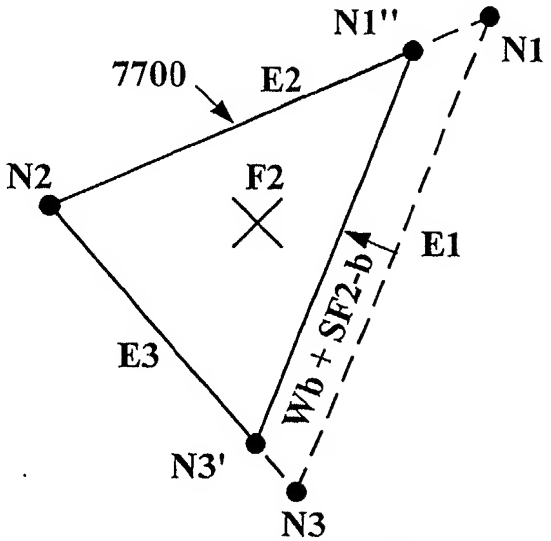
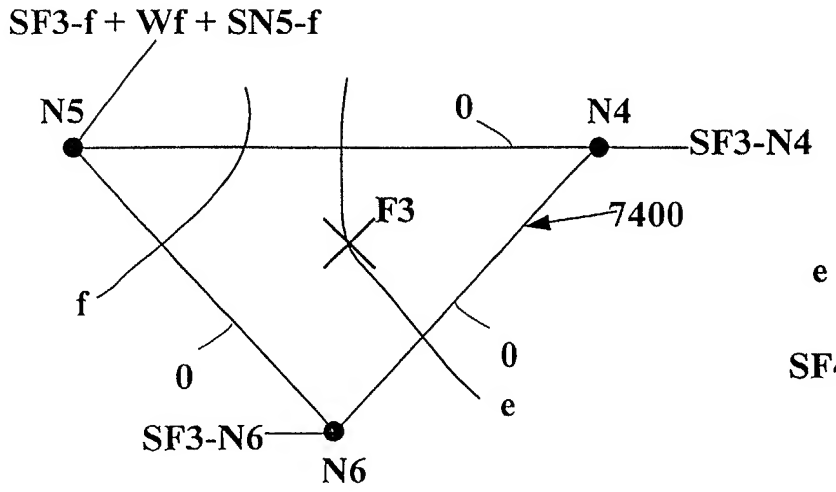


Figure 77



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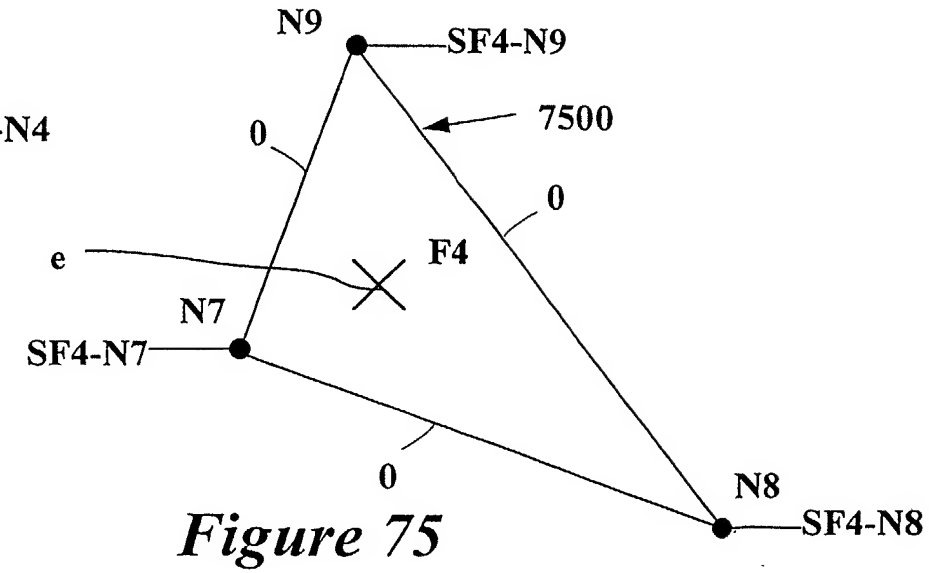


Figure 75

20:07:20 "5609908T

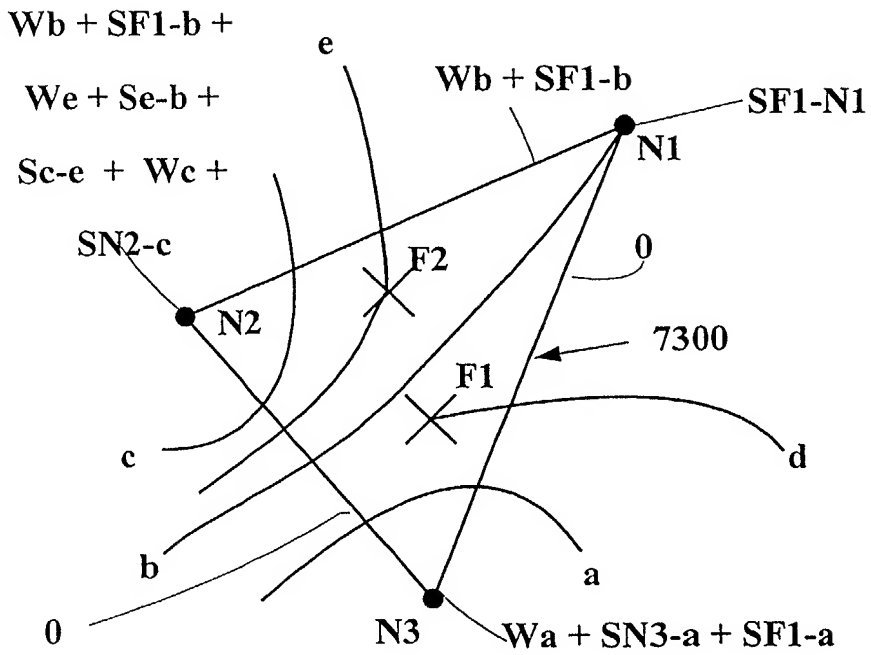


Figure 76

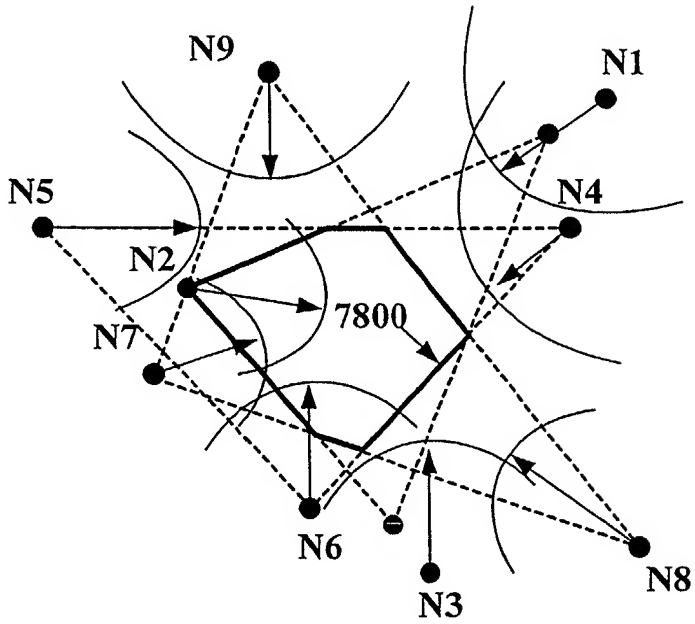


Figure 78

20102O" 5609900T

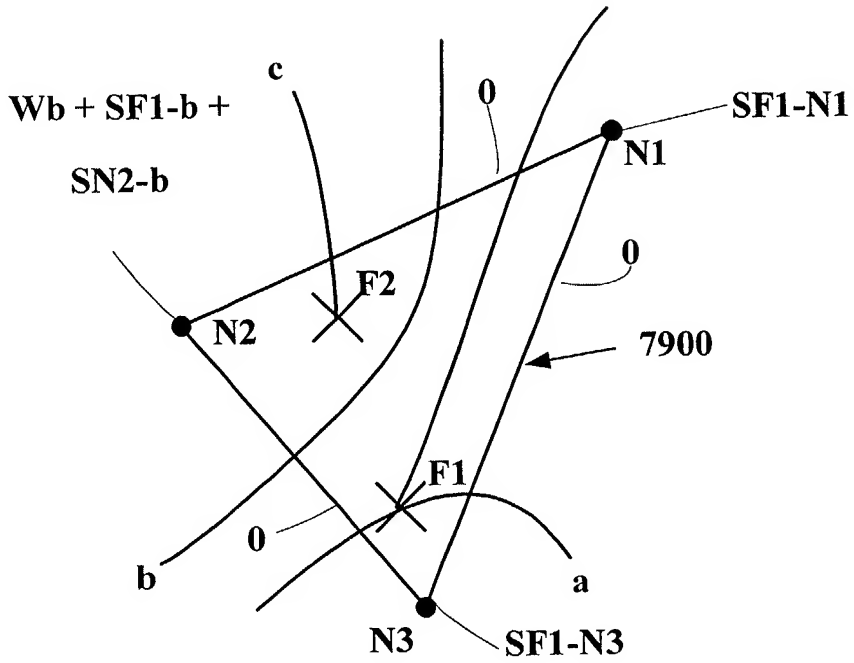


Figure 79

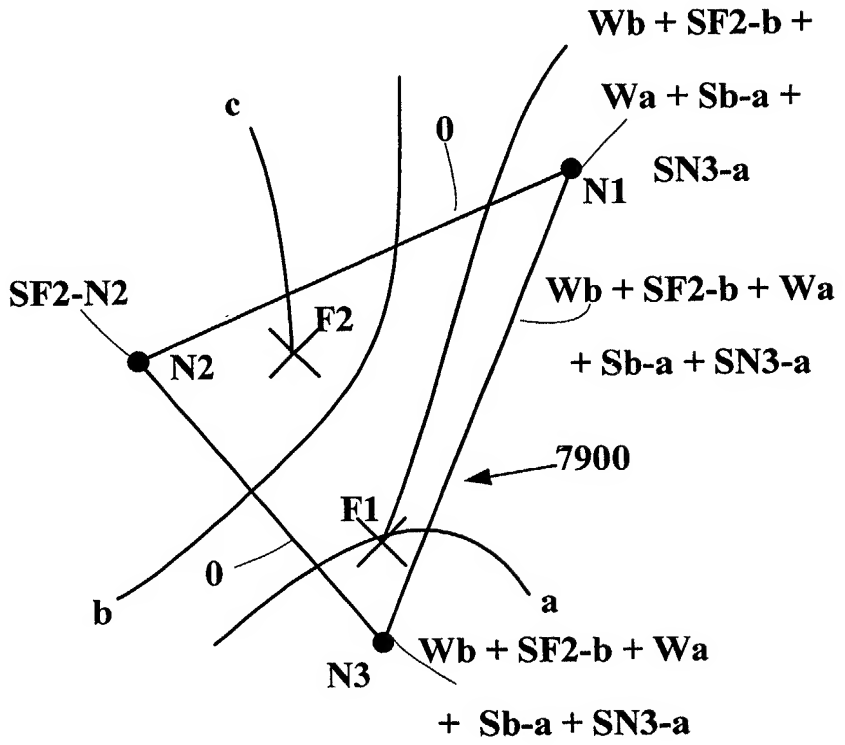


Figure 80

20100205609901

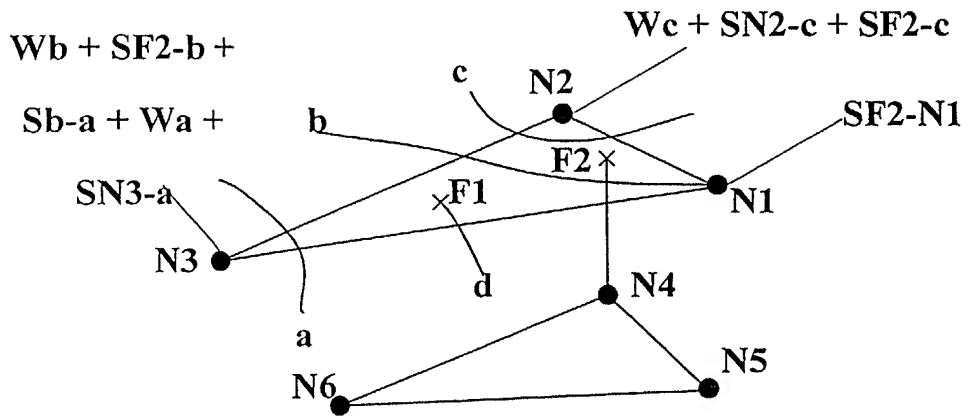
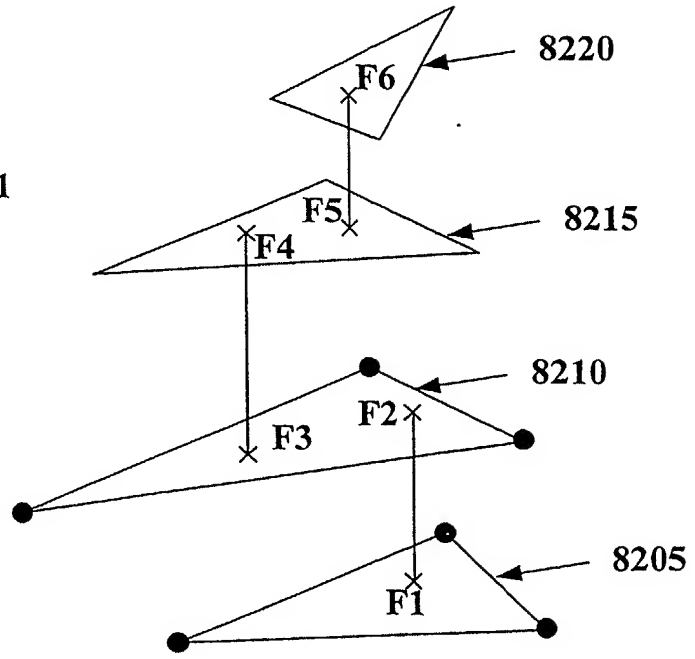


Figure 81



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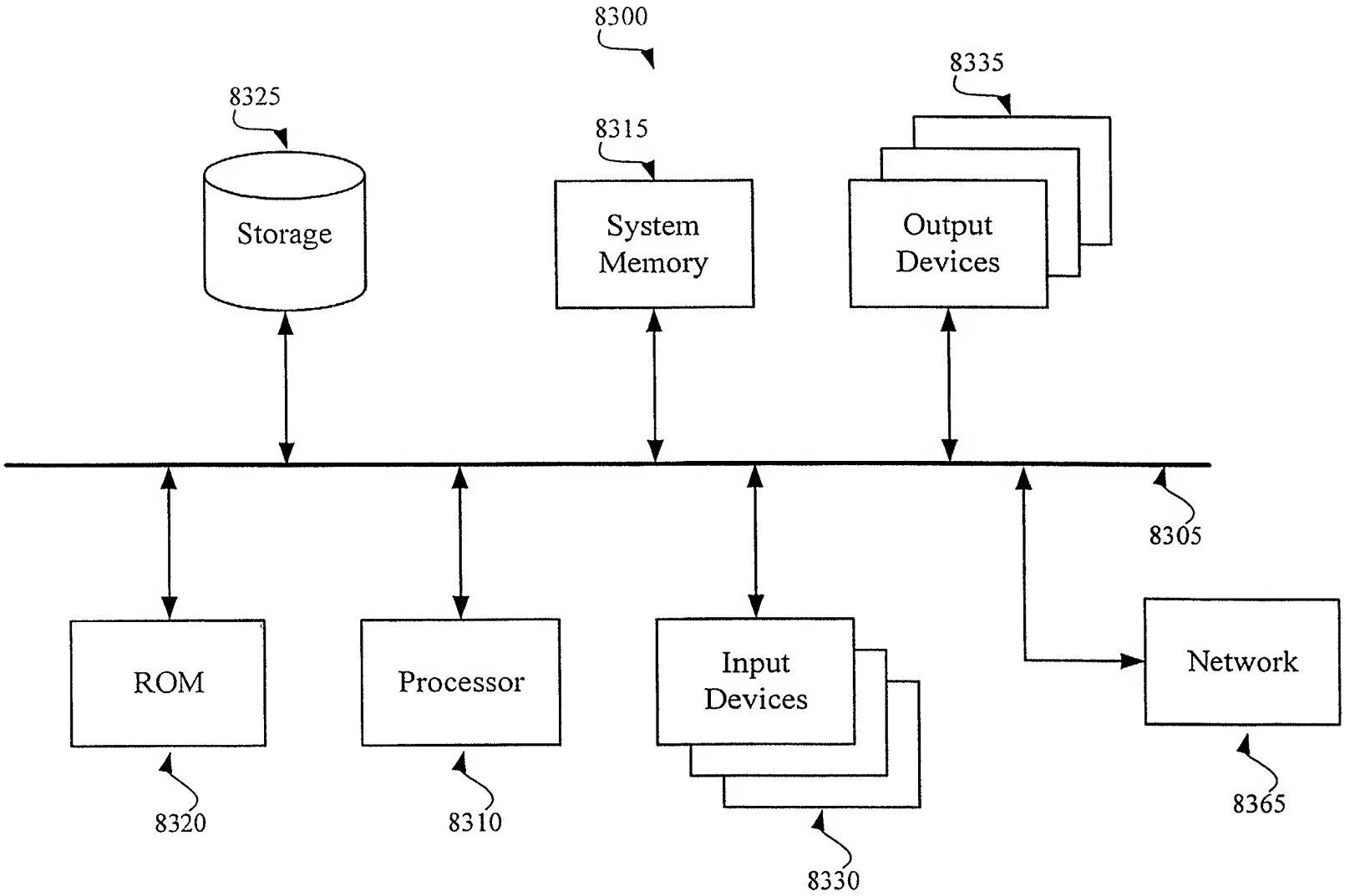
8300
8325
8315
8335
Storage
System Memory
Output Devices
8305
ROM
Processor
Input Devices
Network
8320
8310
8330
8365

Figure 83